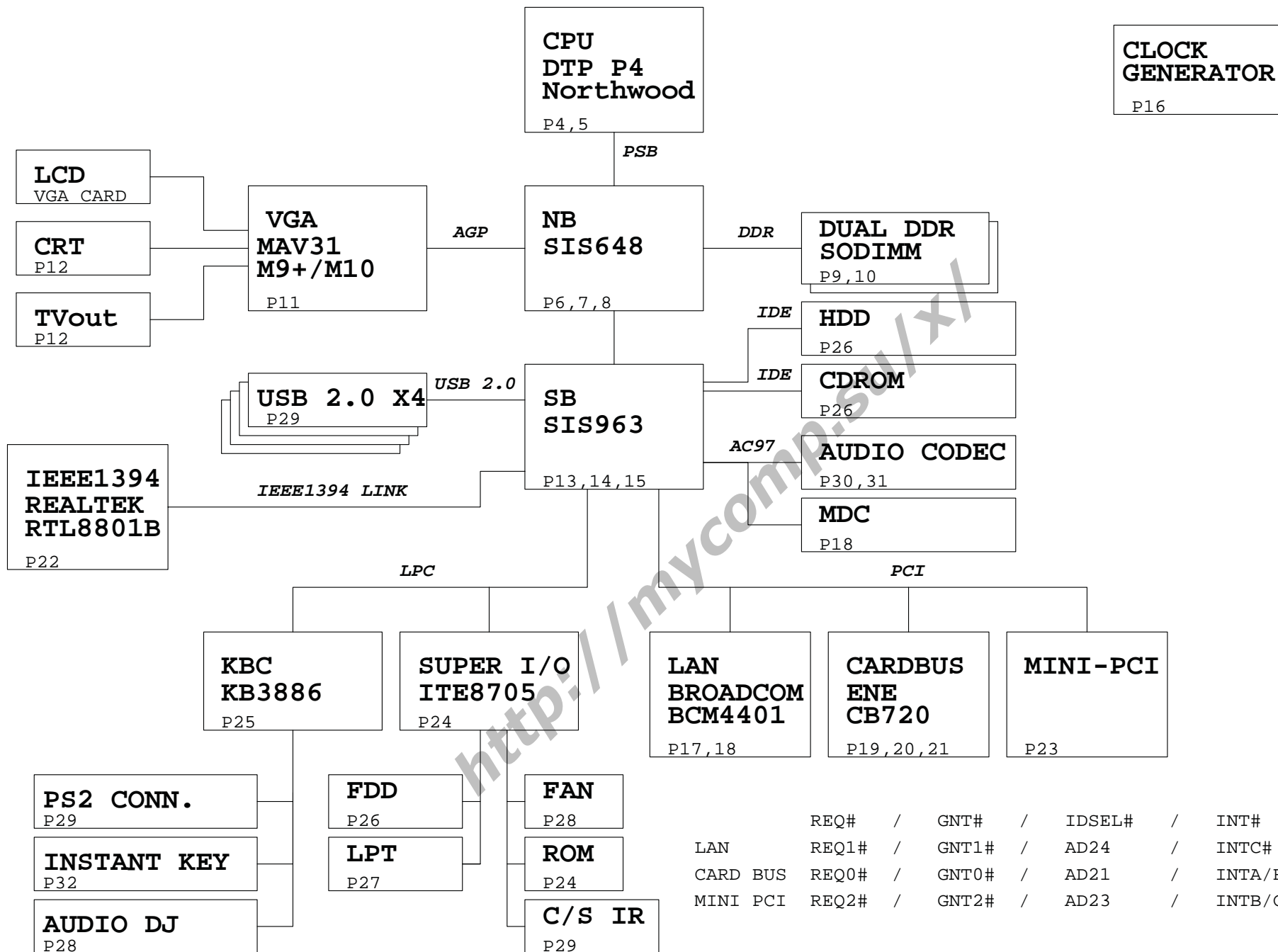


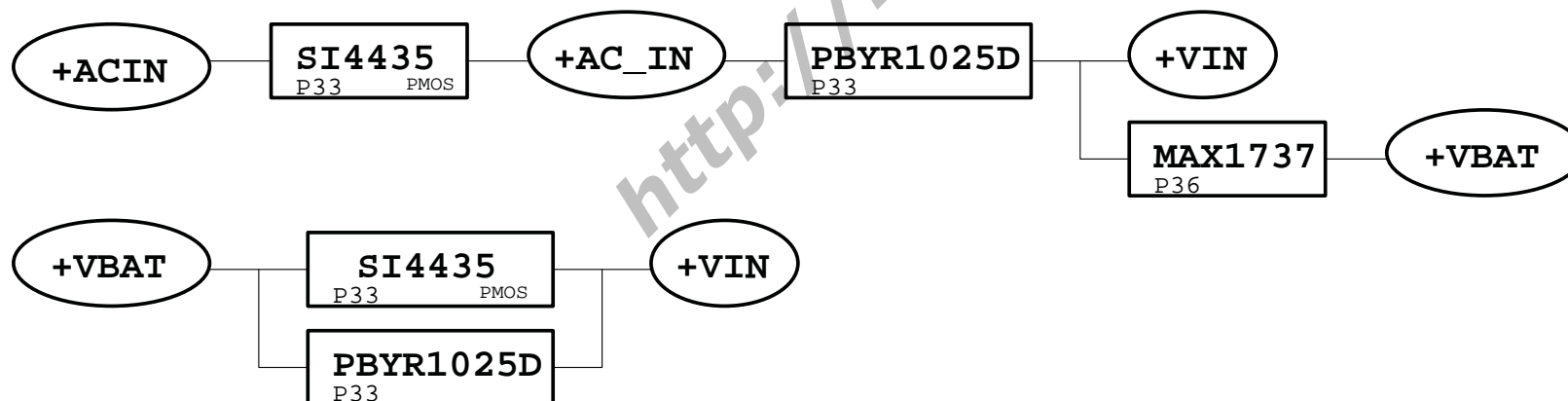
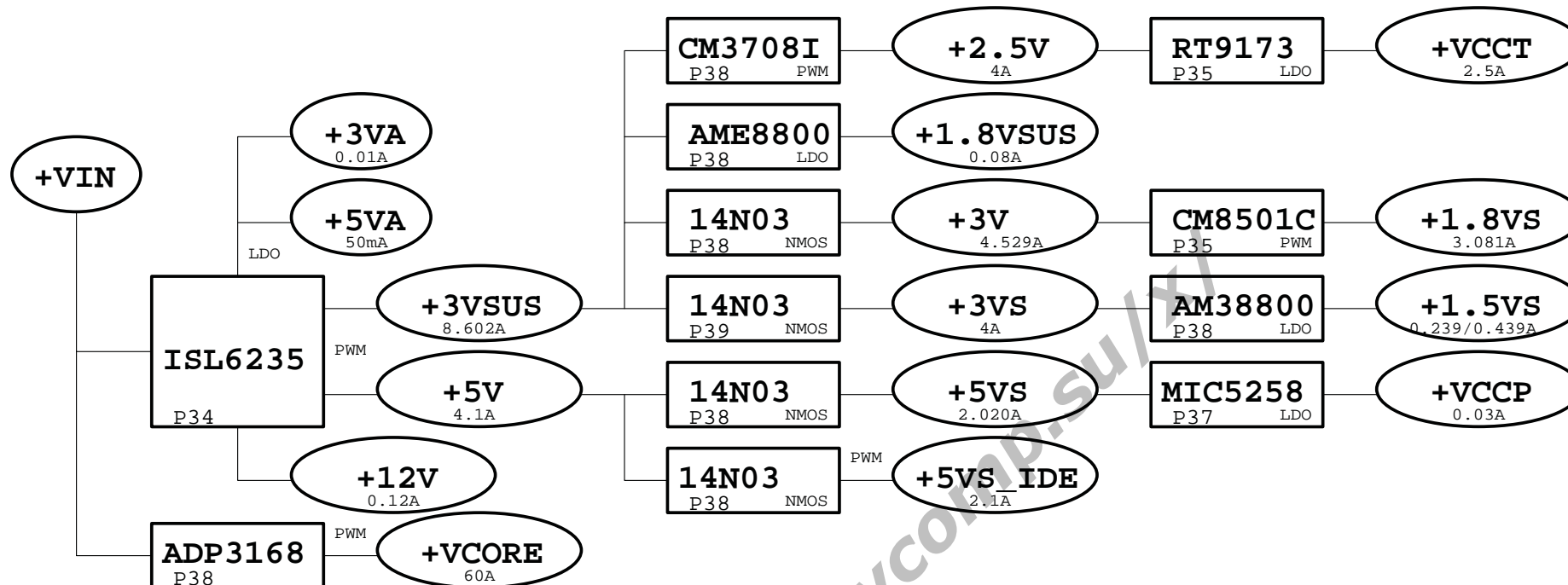
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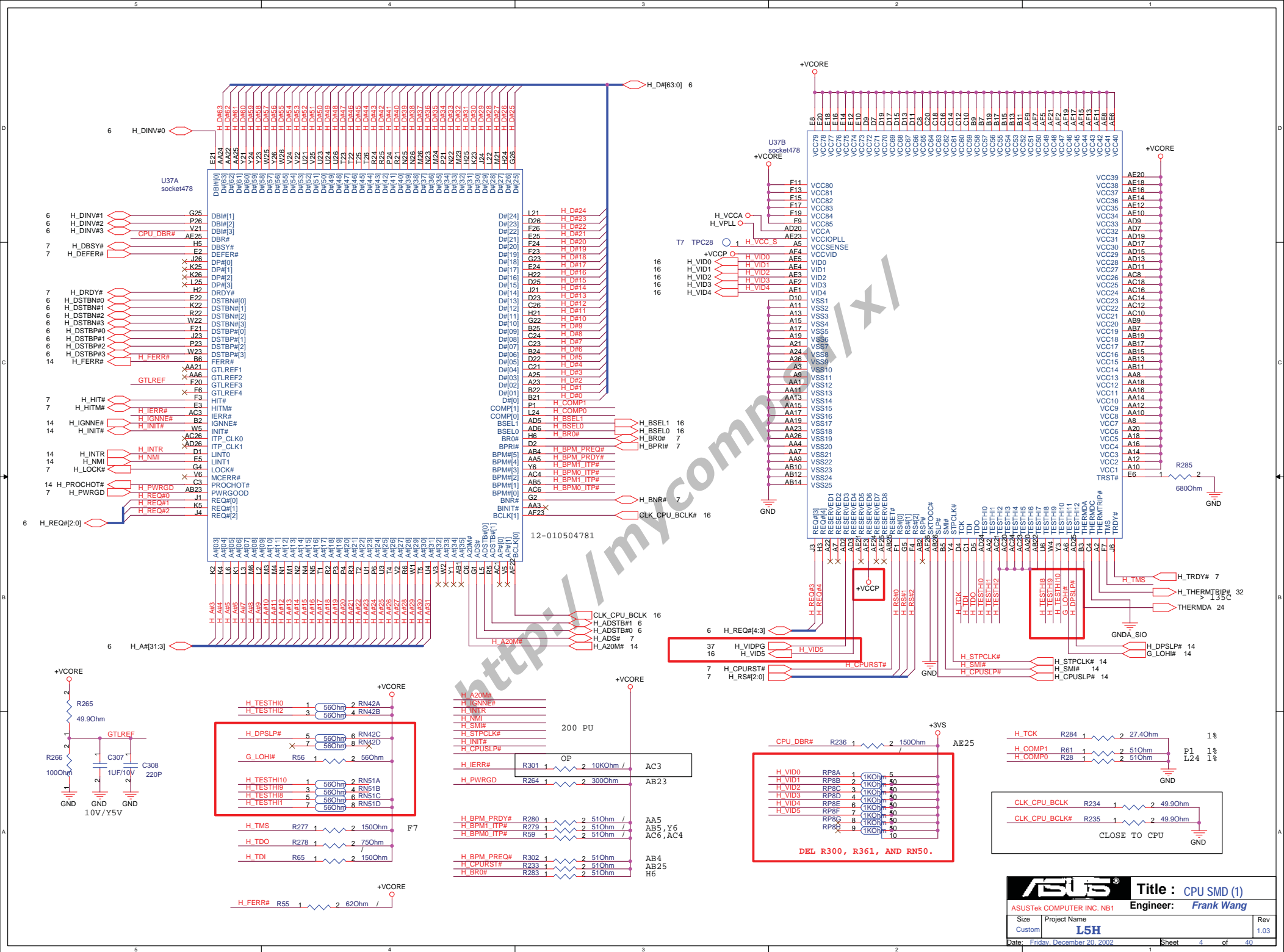
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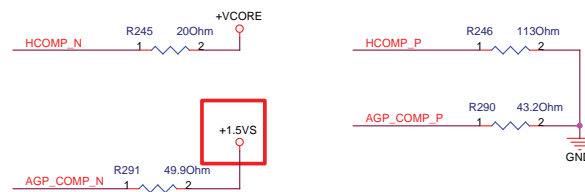
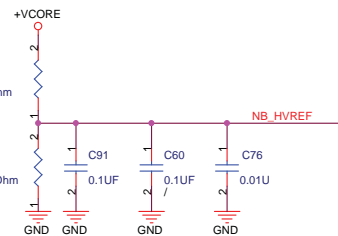
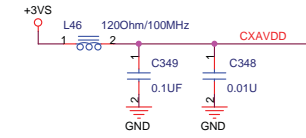
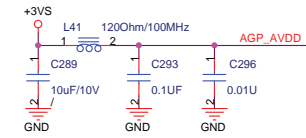
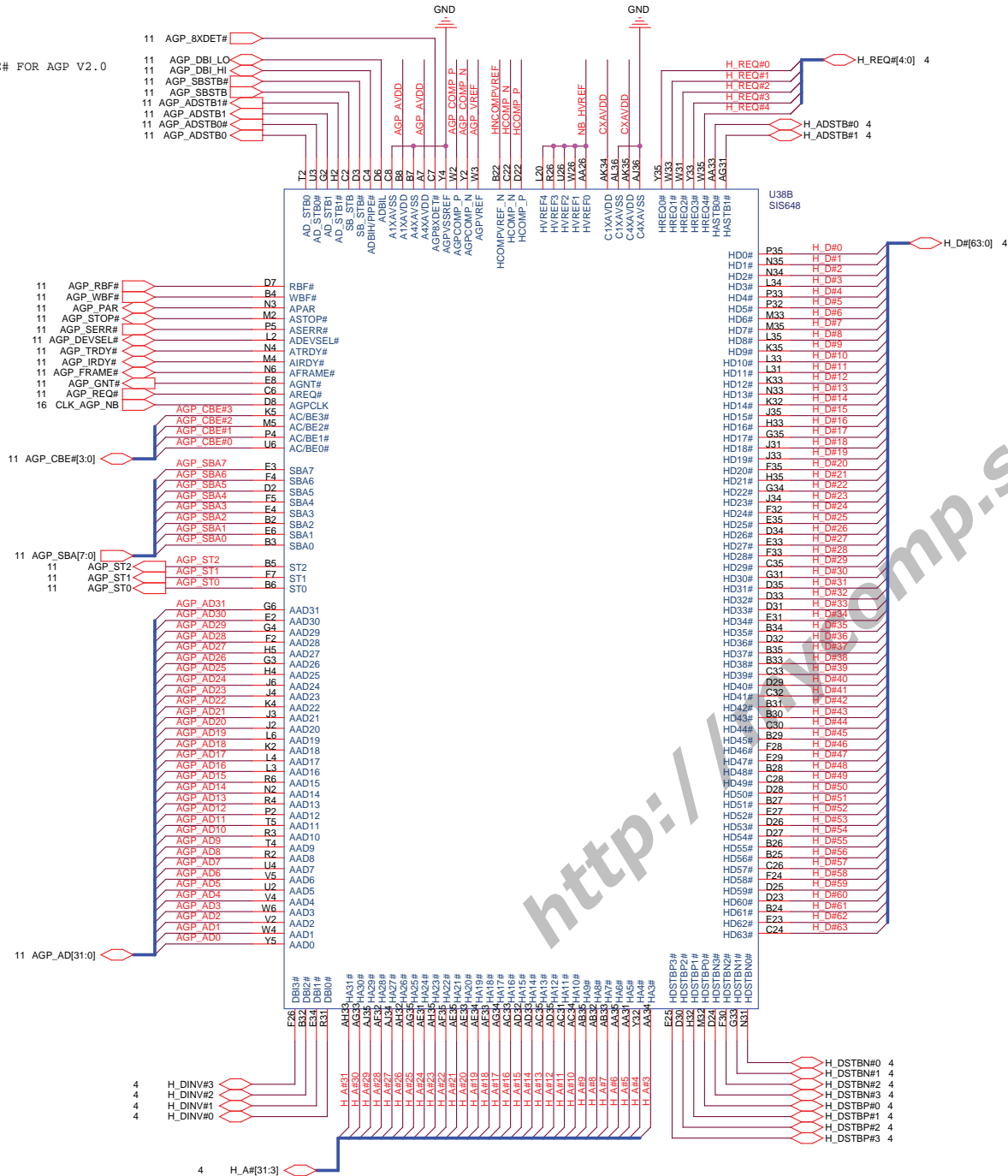


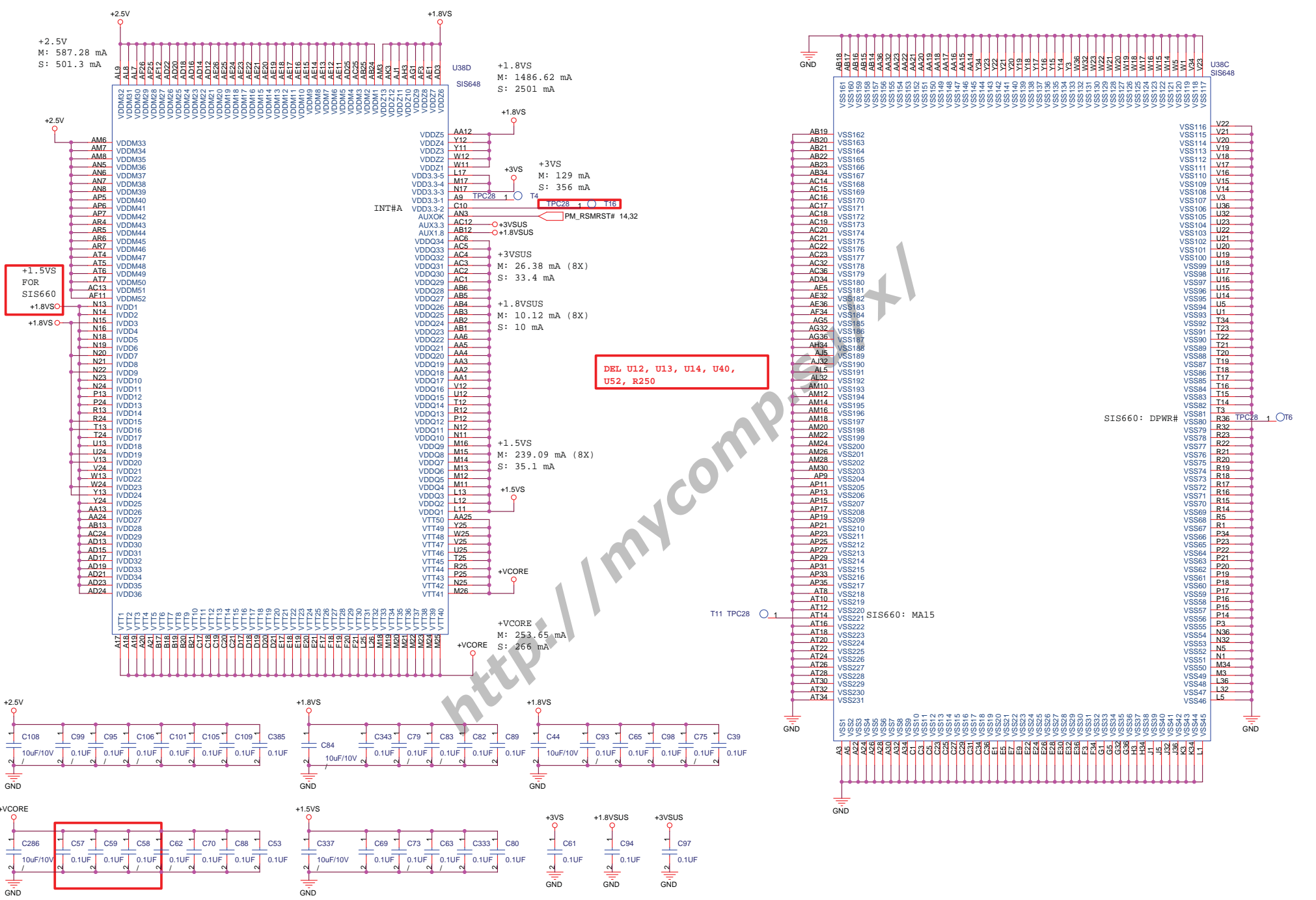
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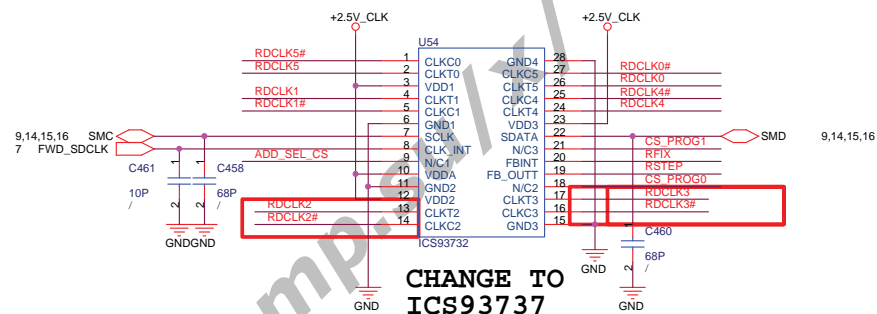
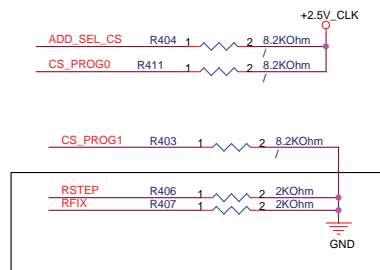
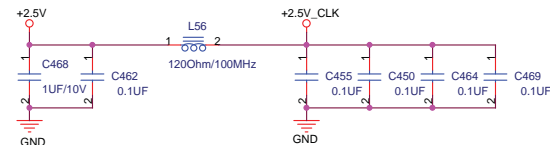
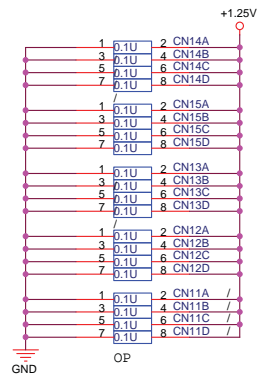
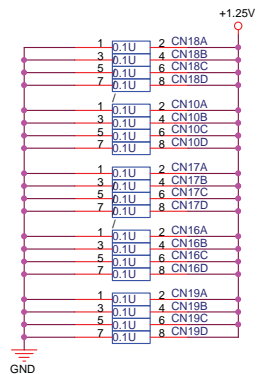
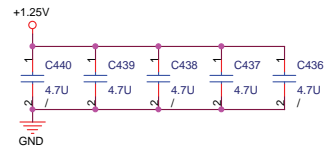
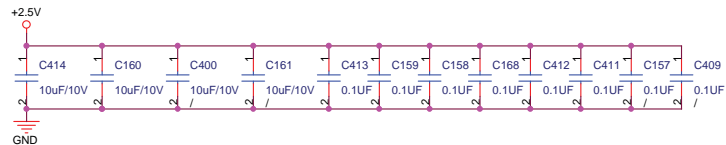


PIPE# FOR AGP V2.0



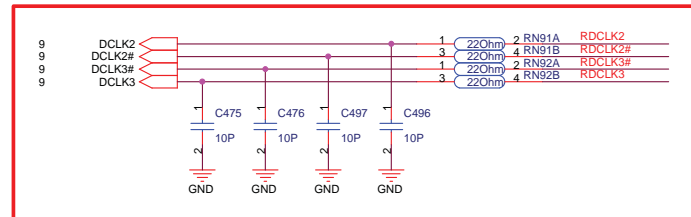
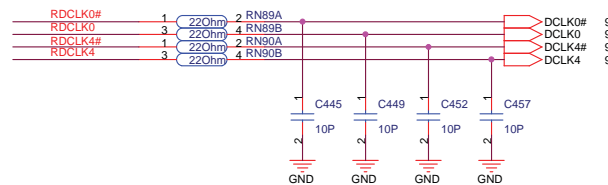
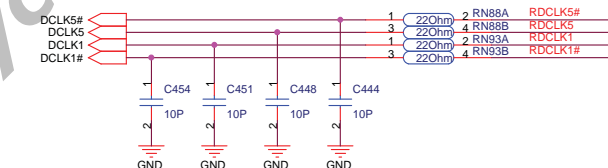


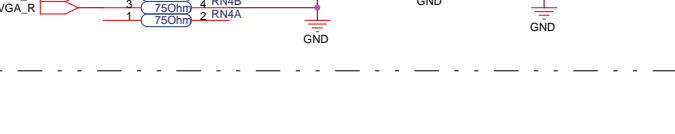
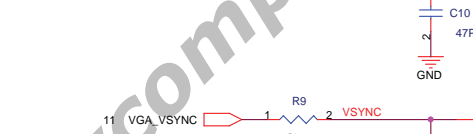
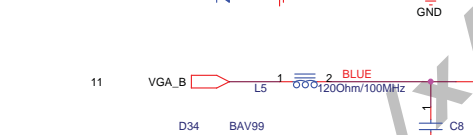
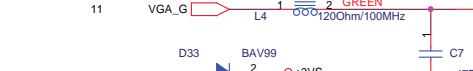
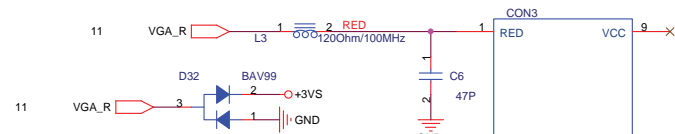
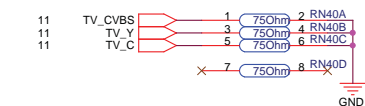
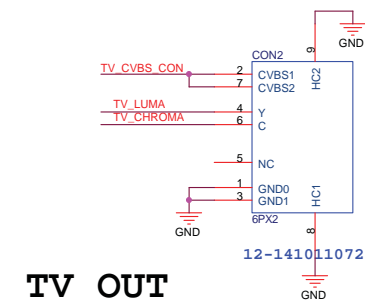
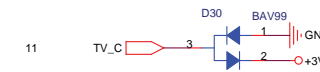
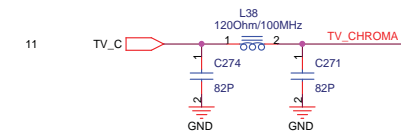
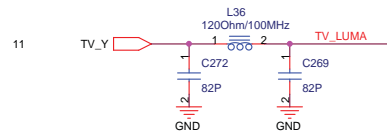
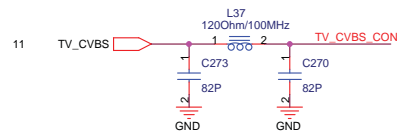
Close to DDR socket



CHANGE TO
ICS93737

PIN9: I2C ADD SEL **
PIN18: CS_PROG0 **
PIN19: RSTEP
PIN20: FIX
PIN21: CS_PROG1 *

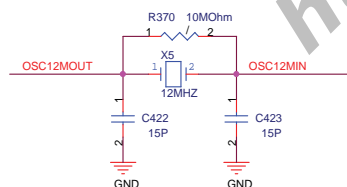
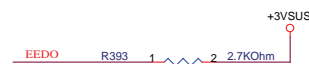
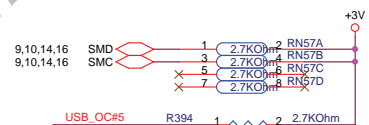
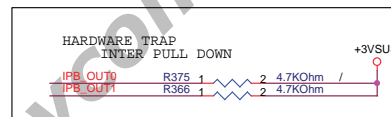
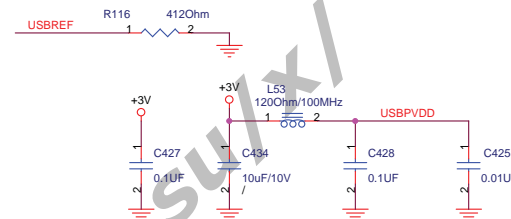
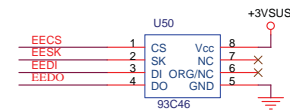
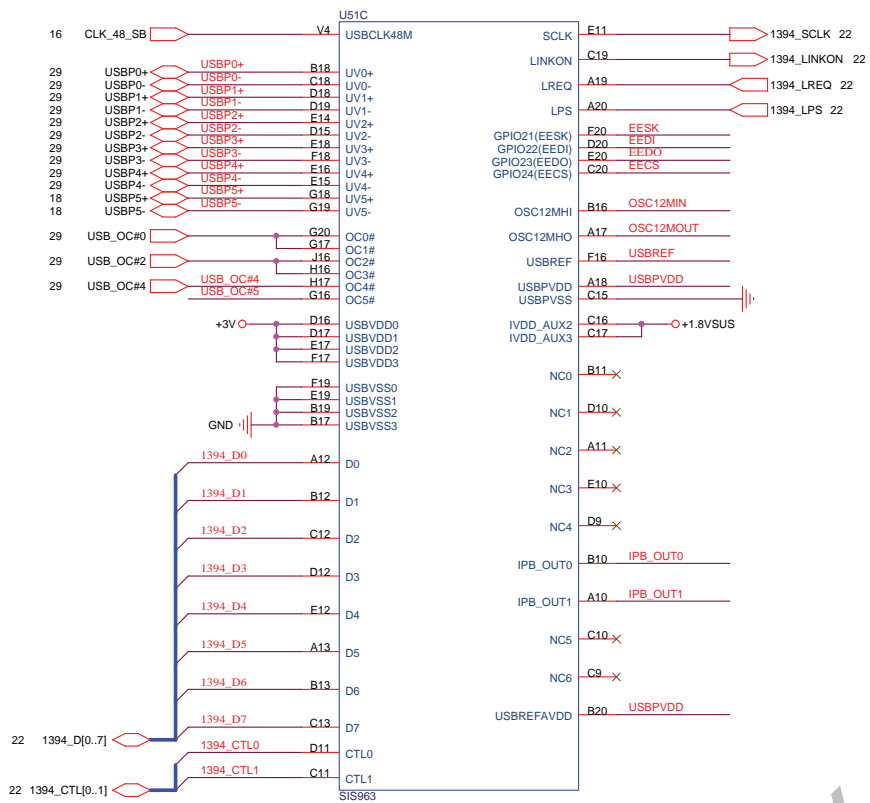




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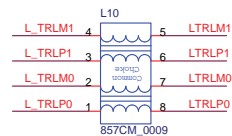
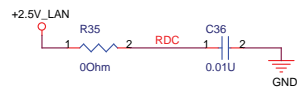
CRT
PIN

DEL U1, U2, U3, U4, U34, U35, U36

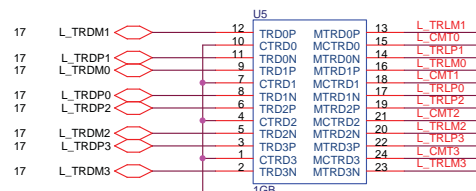
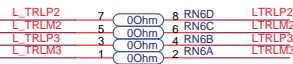
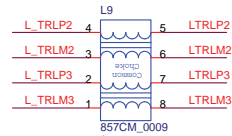


For USB

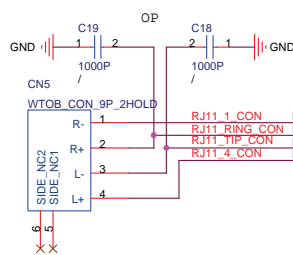
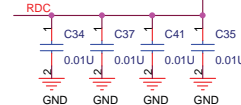
DEL U47, T24, T27



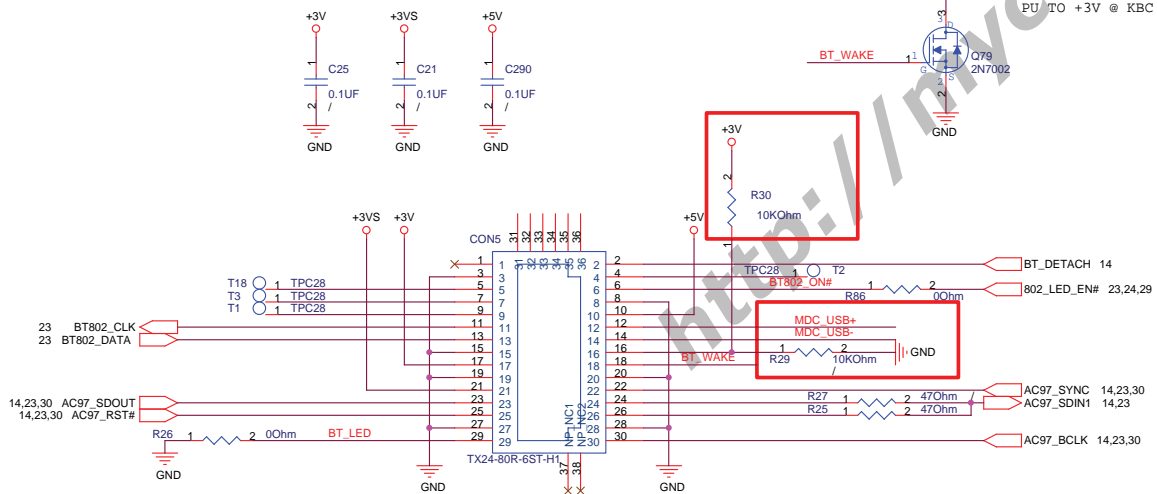
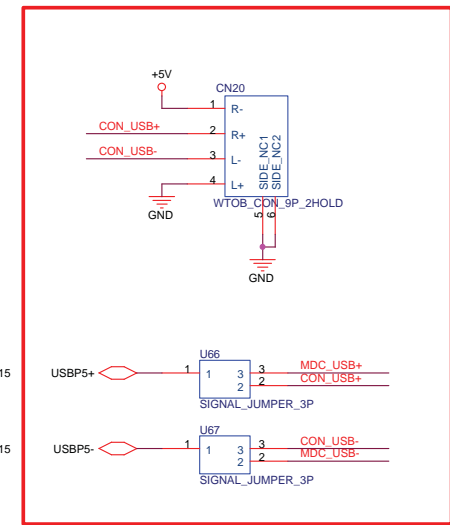
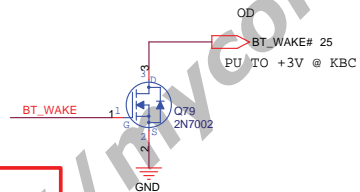
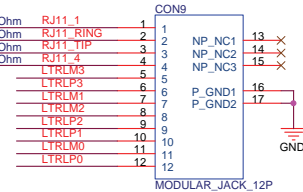
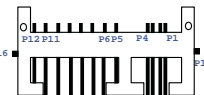
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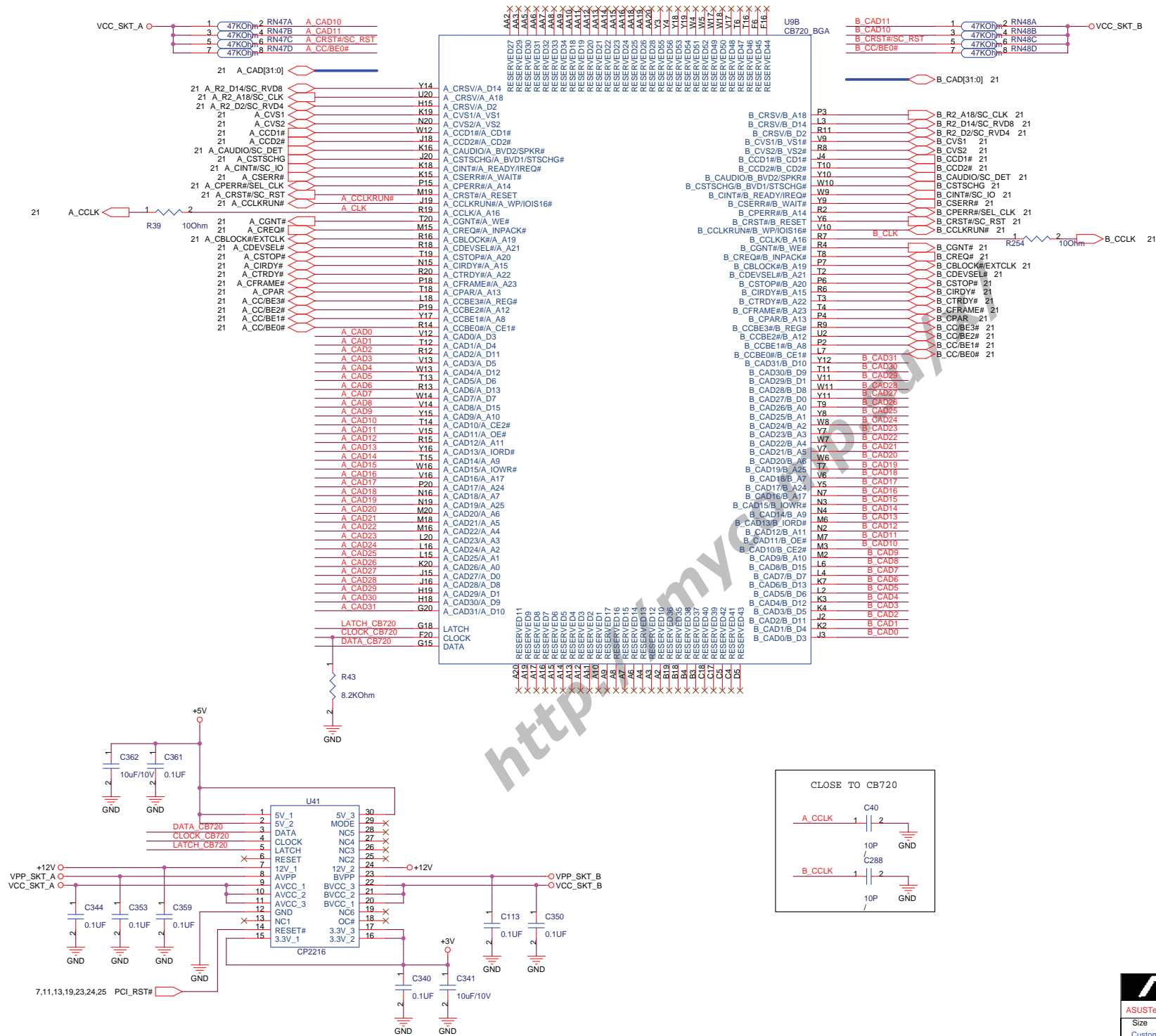


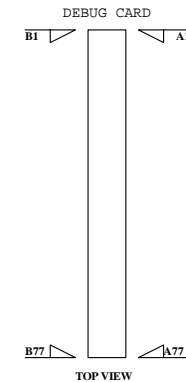
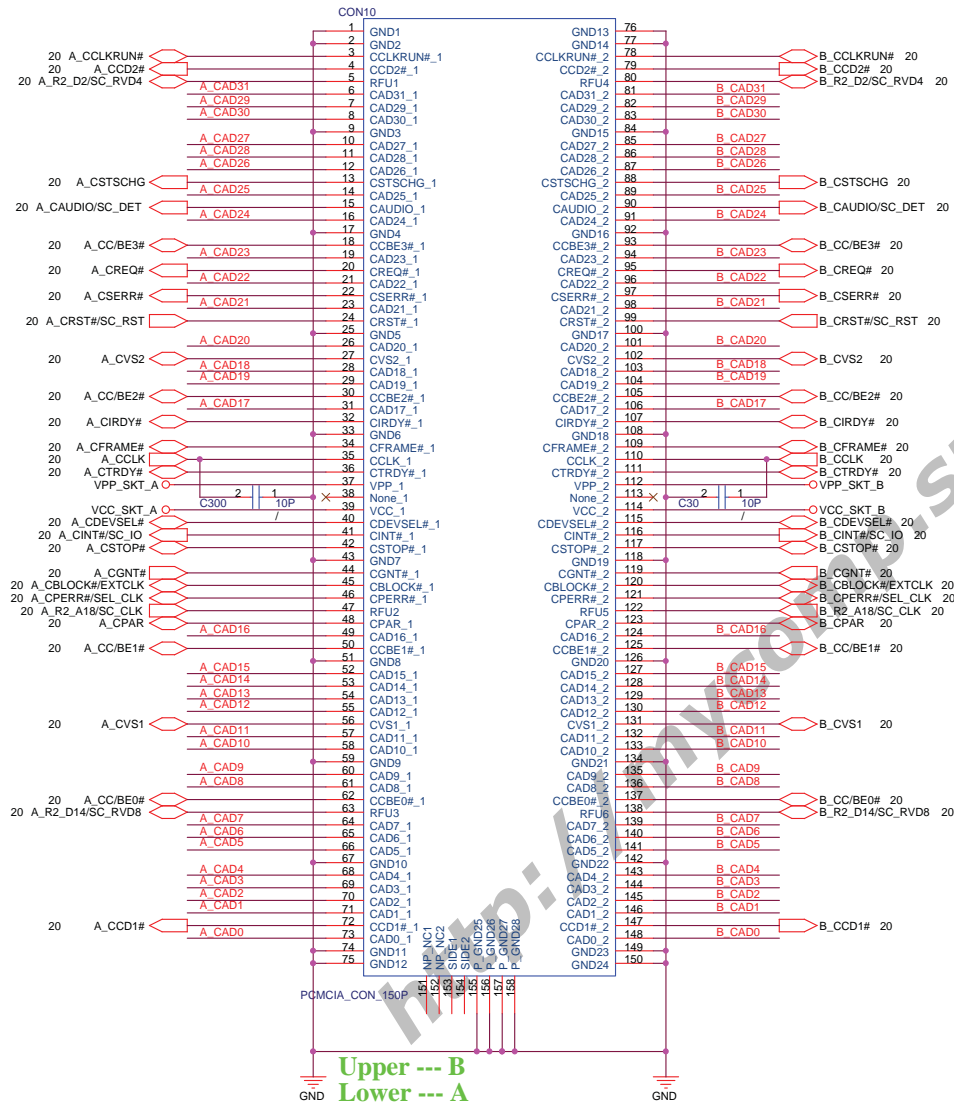
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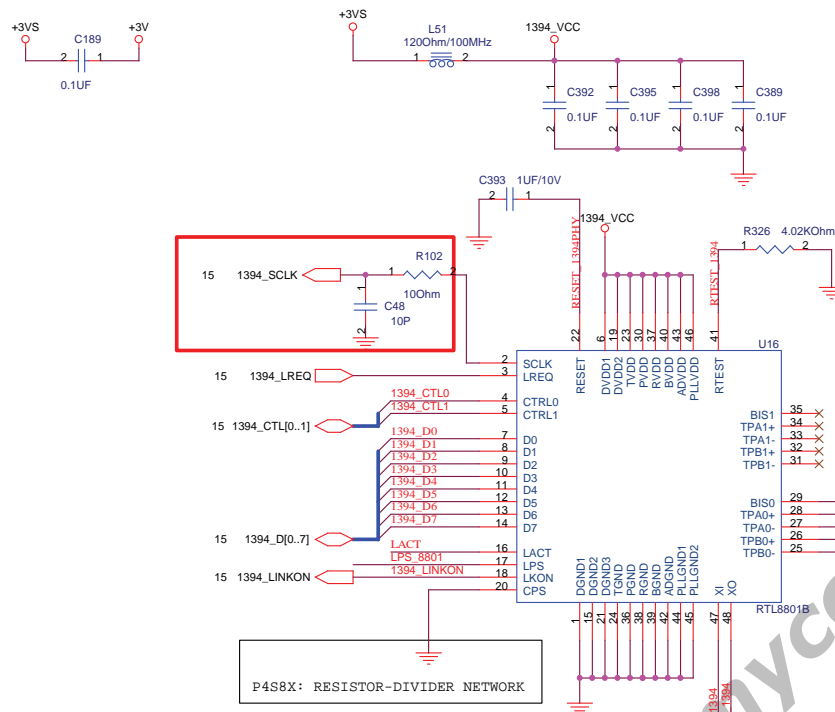


RJ 45 & RJ 11 P16
BOTTOM VIEW

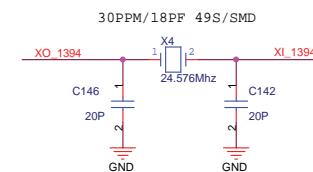
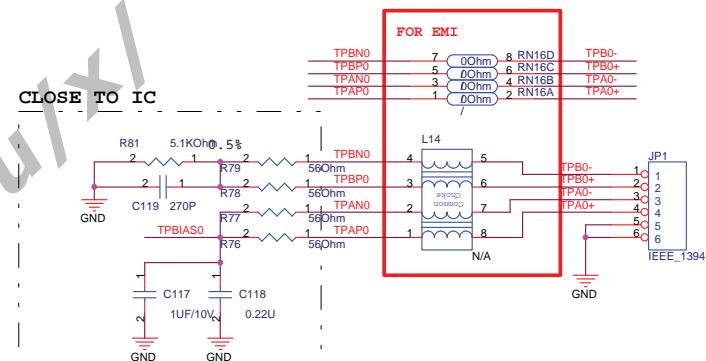
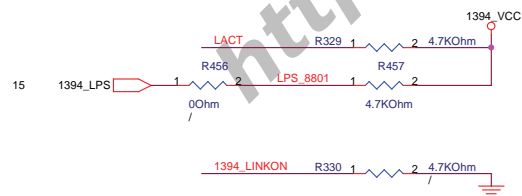


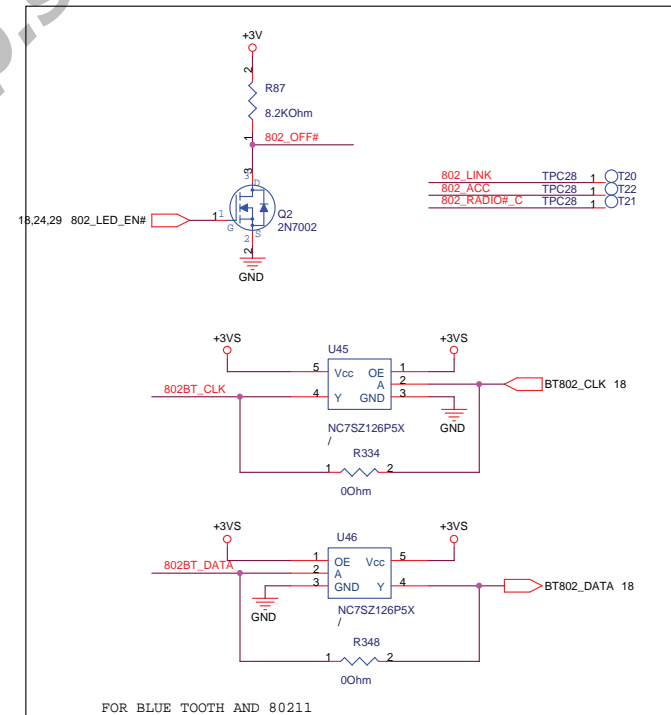
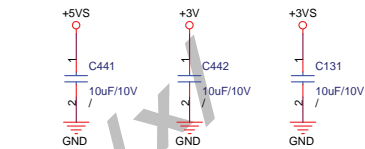
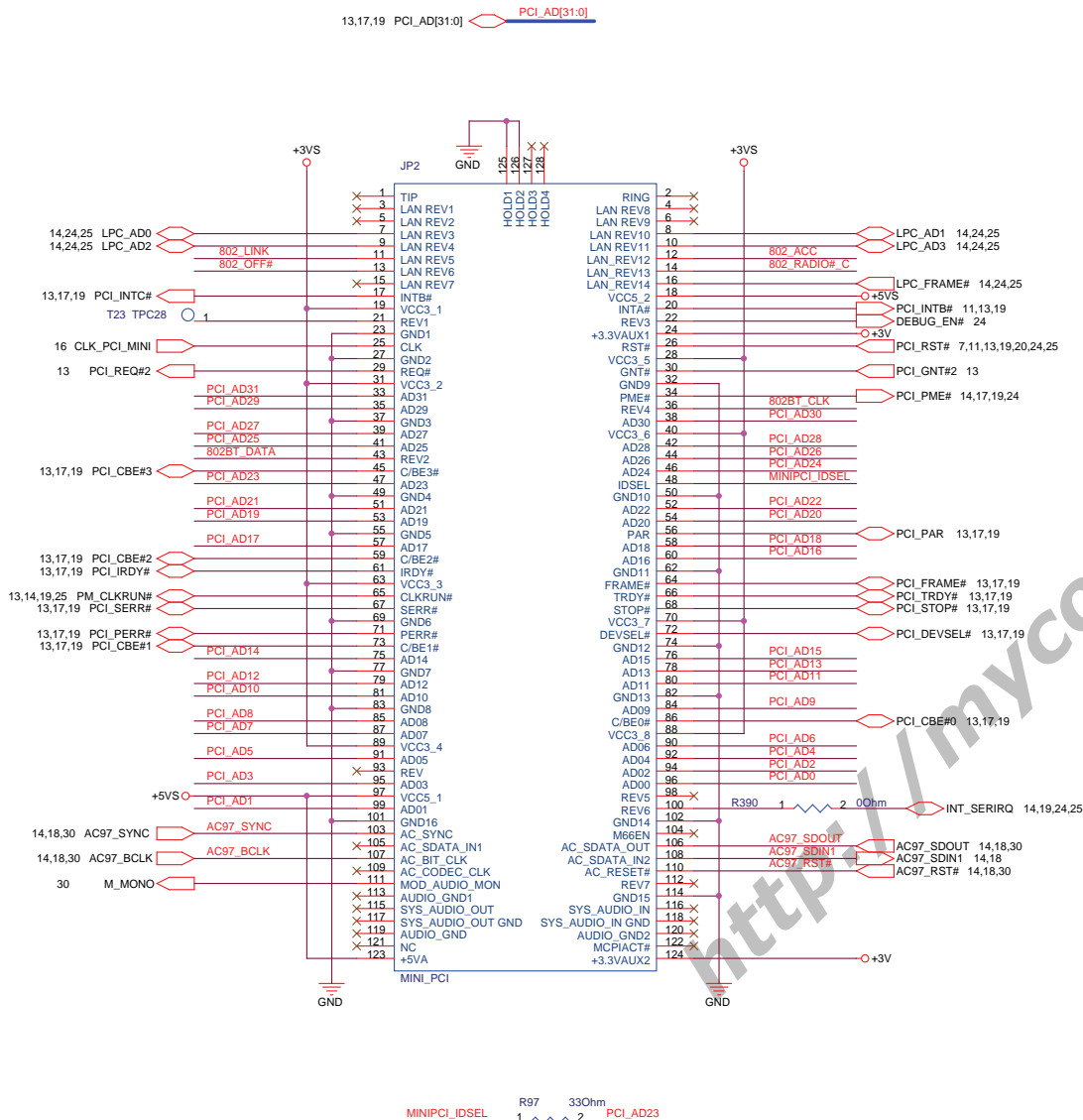


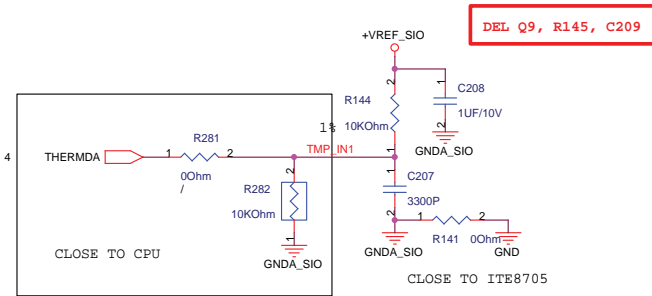
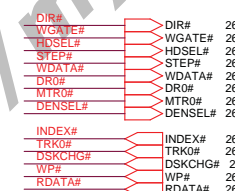
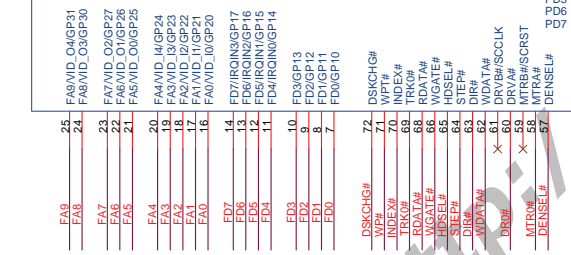
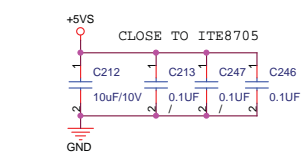
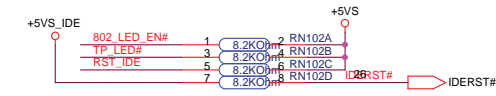
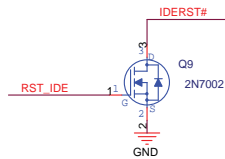
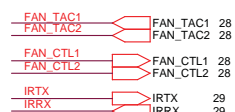
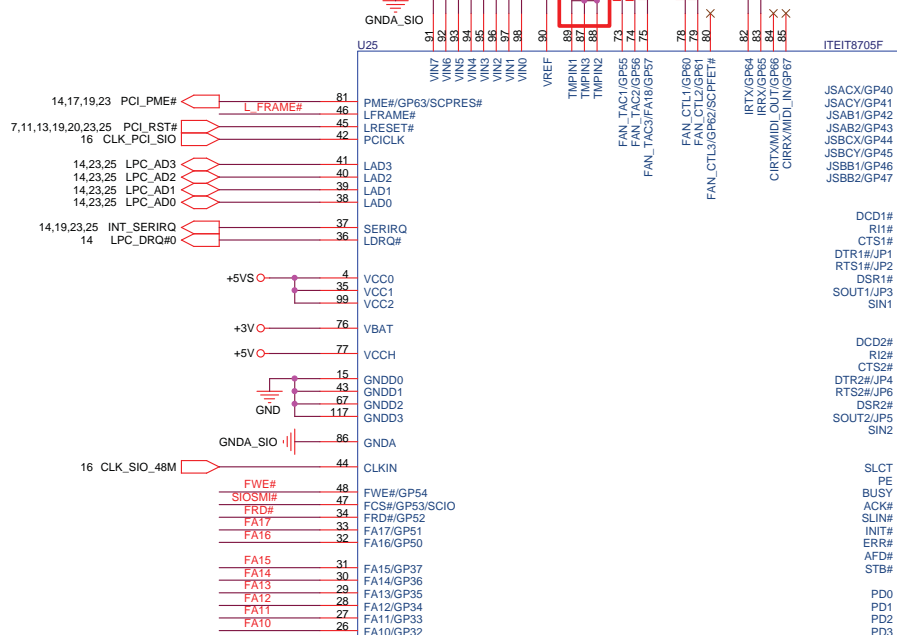
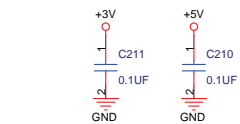




RTL8801B 12pF inside

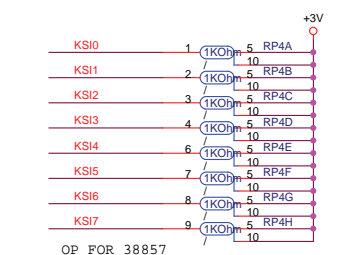
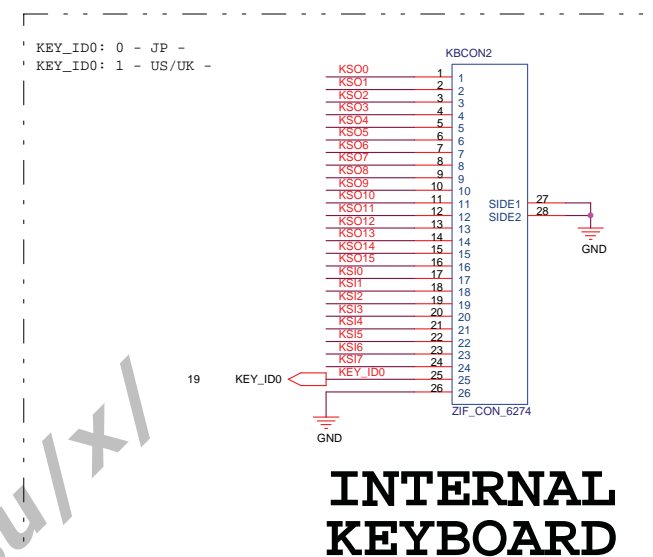
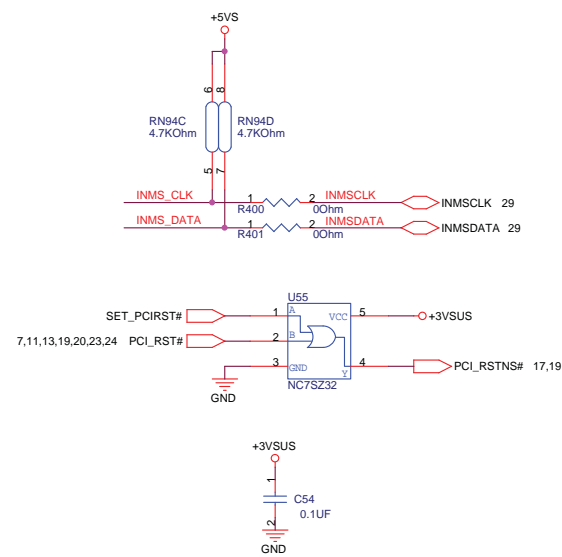
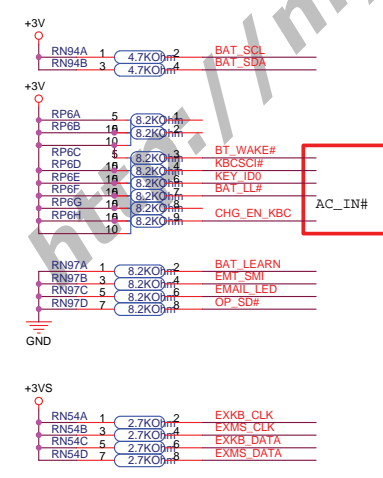
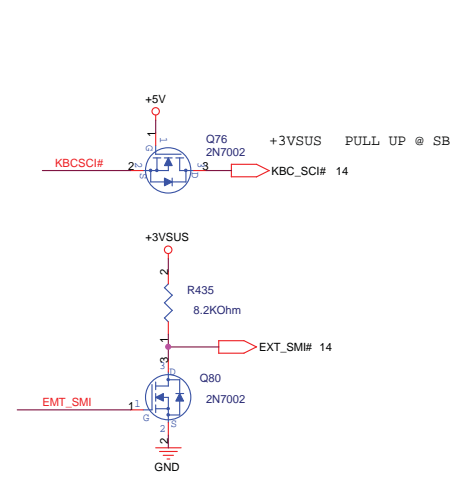
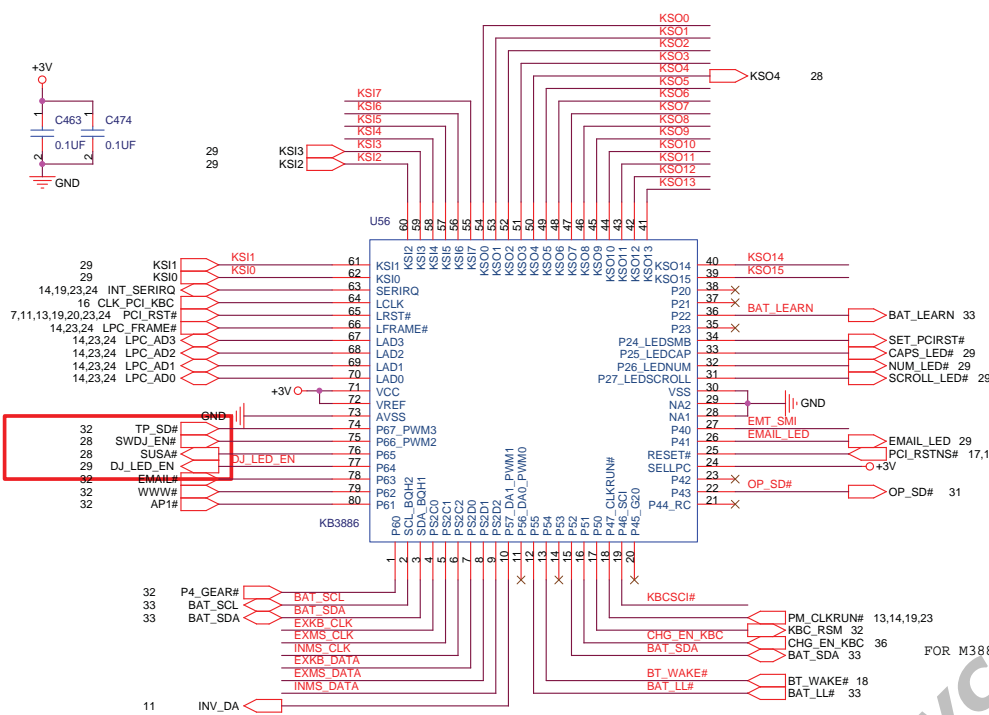


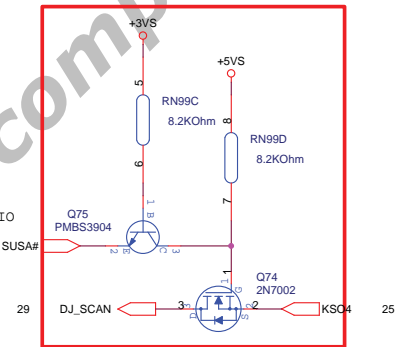
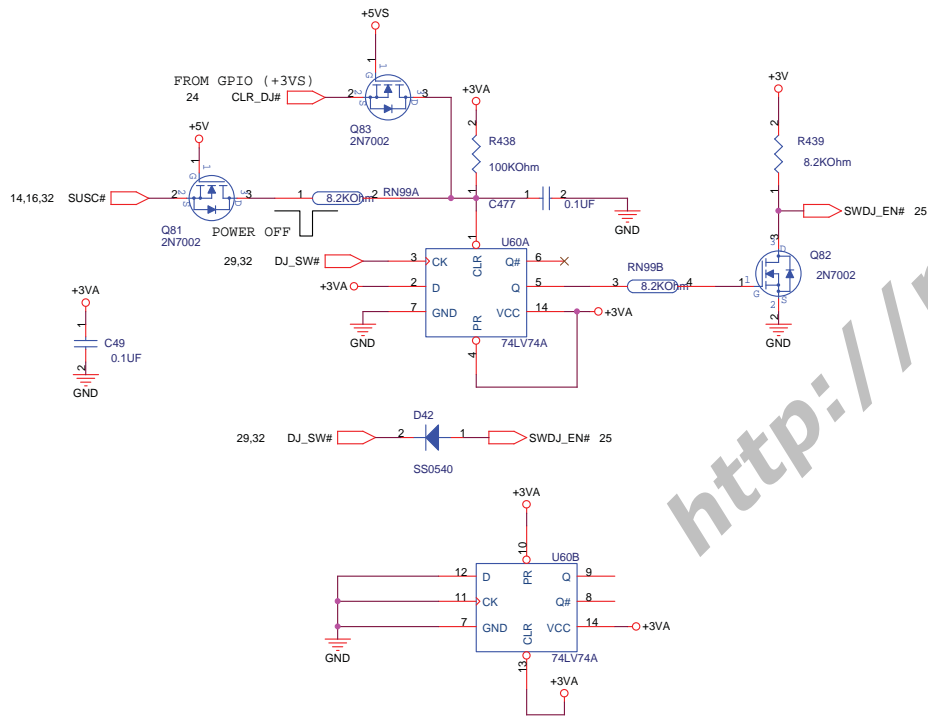
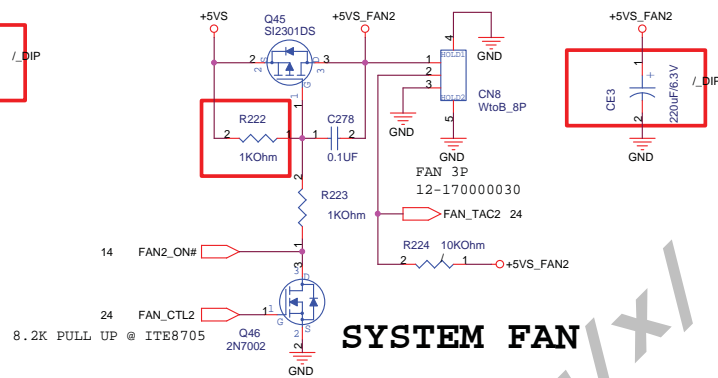
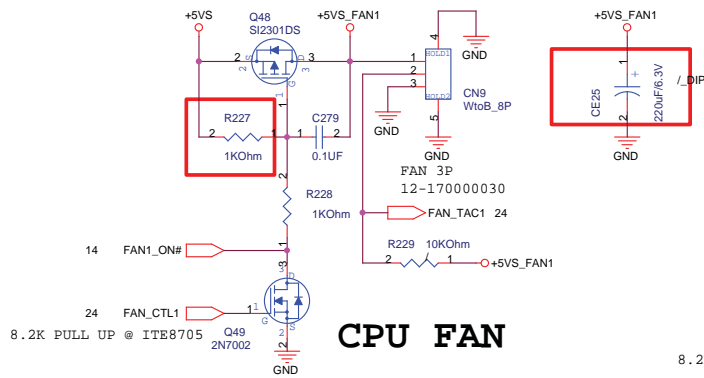


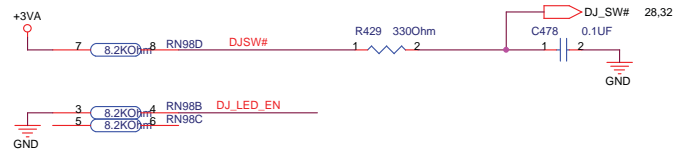
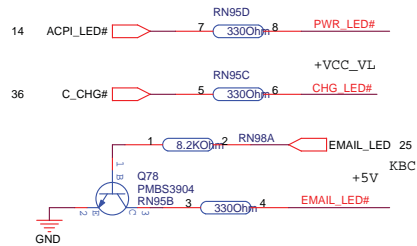
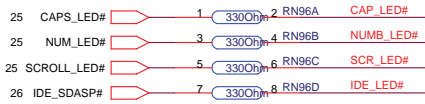


DEL Q9, R145, C209

ISA ROM

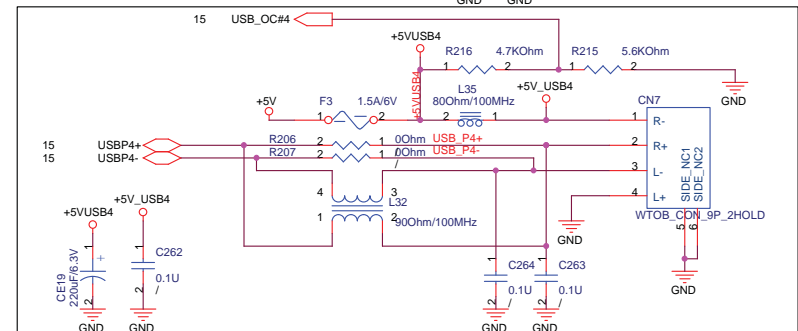
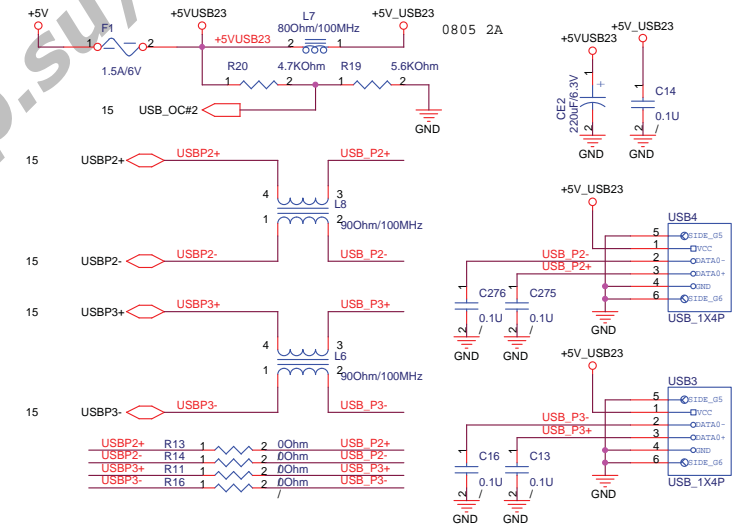
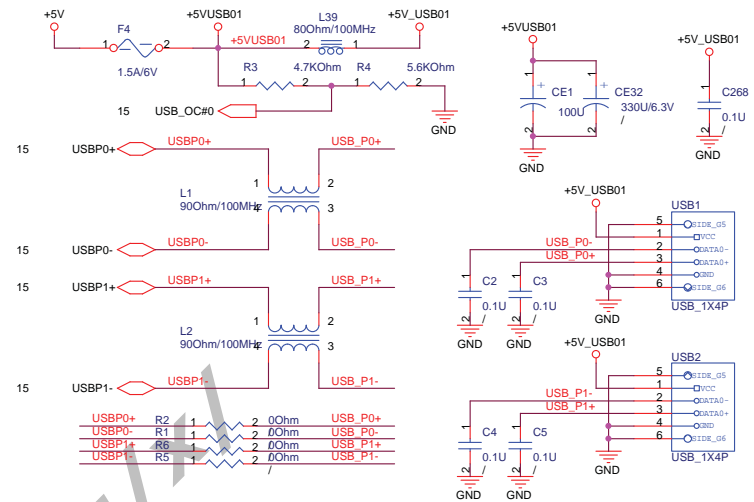
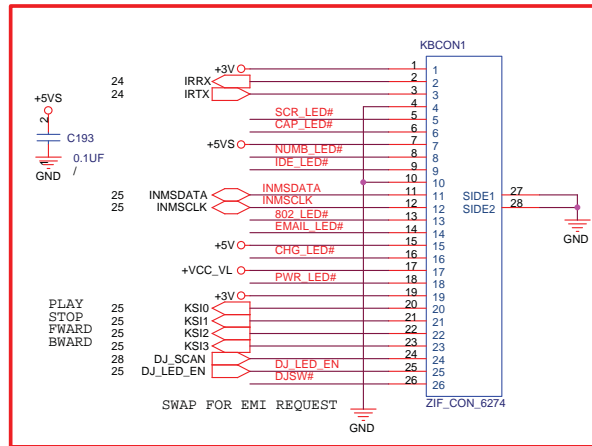


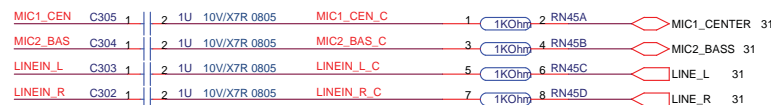
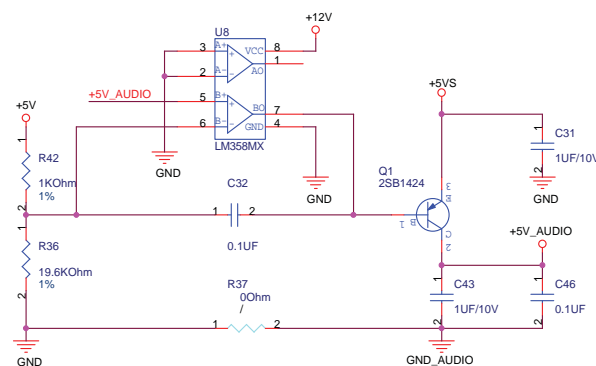
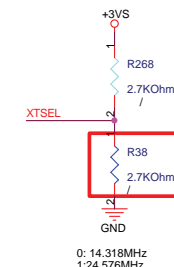
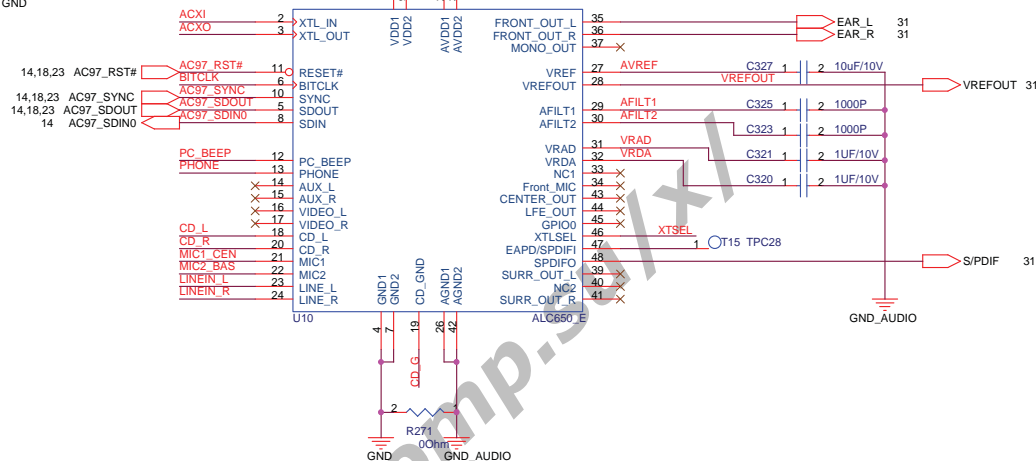
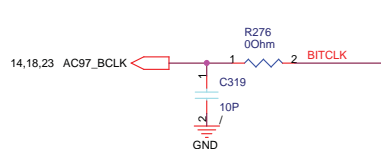
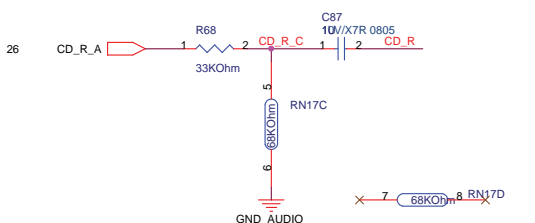
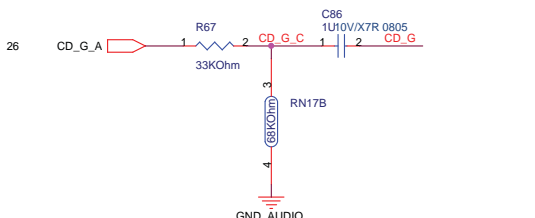
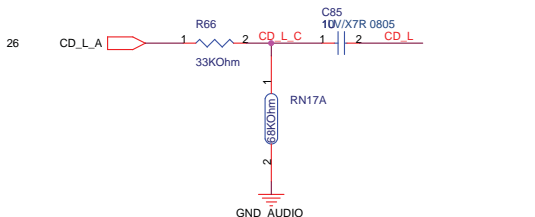
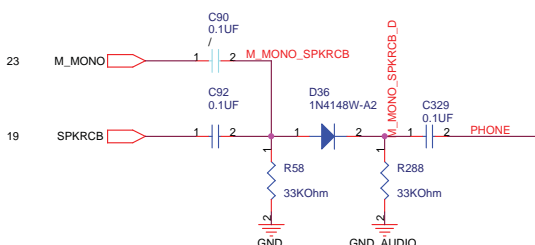
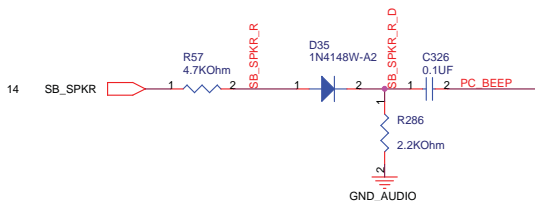
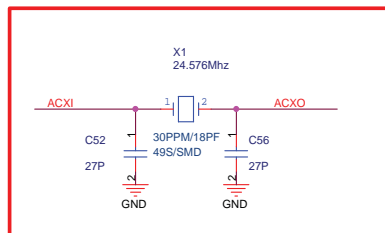


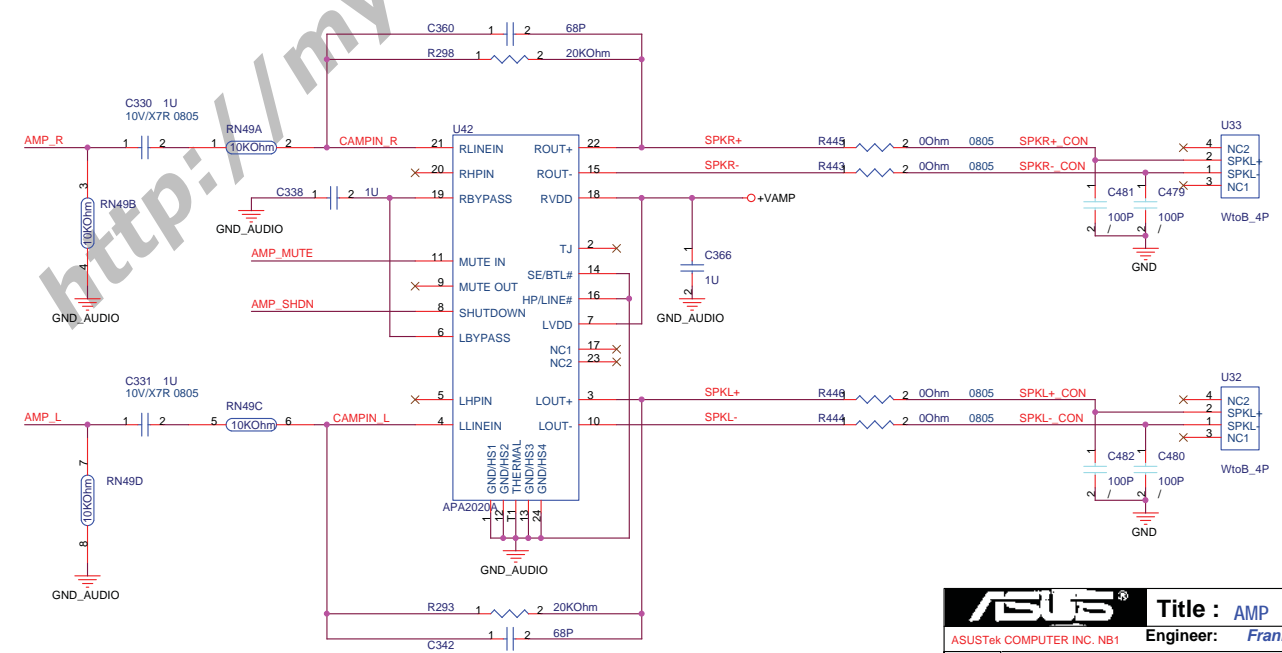
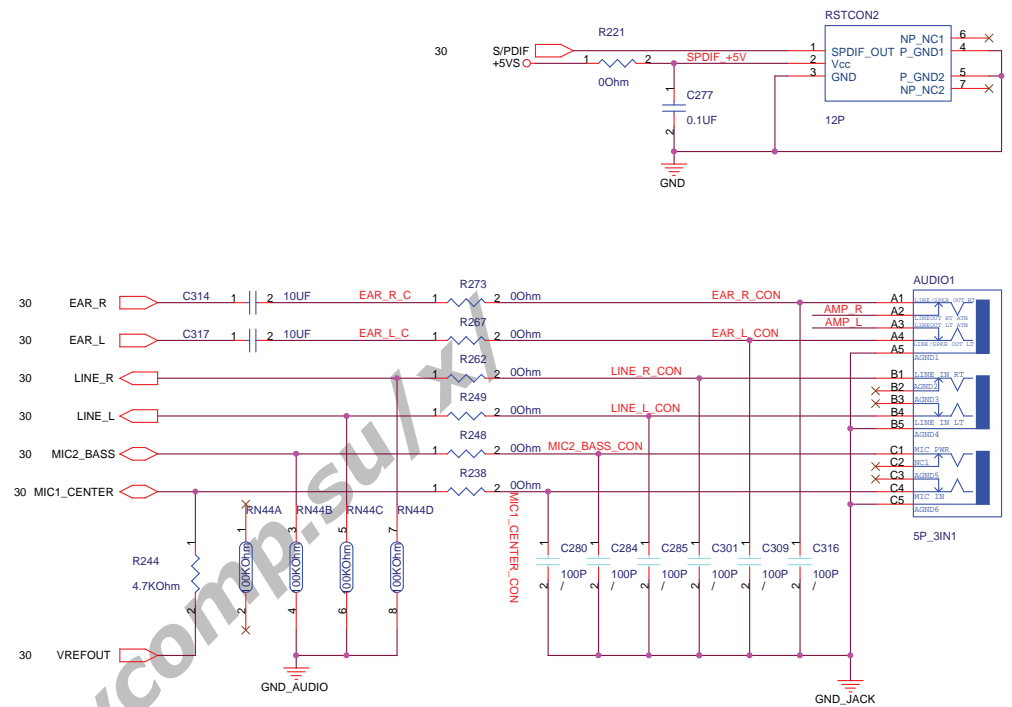
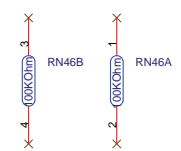
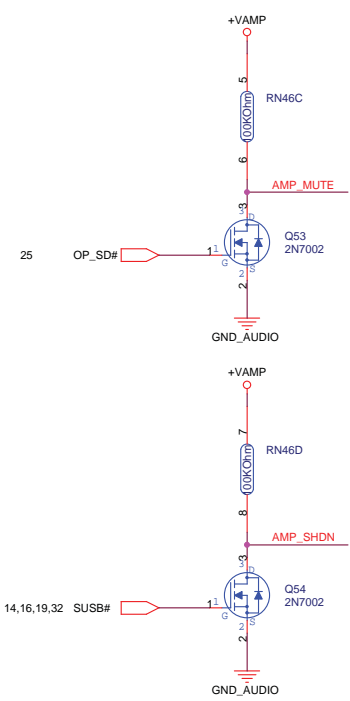
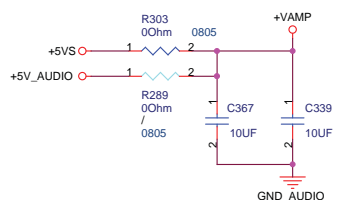
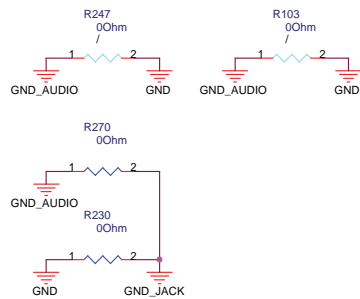


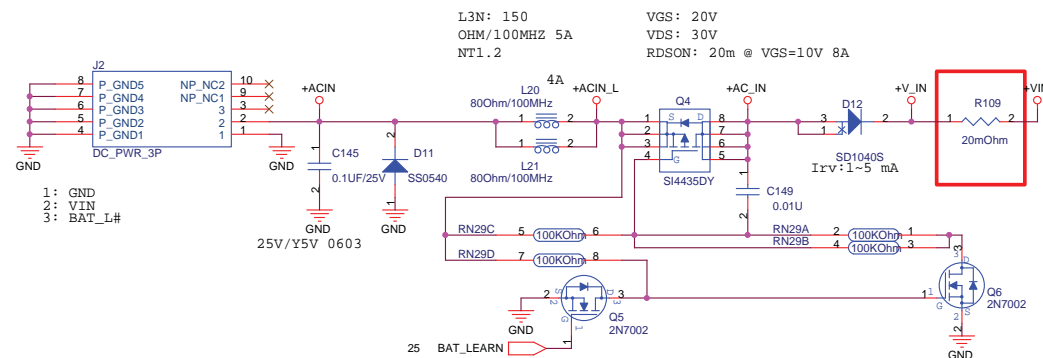
DEL RN91, RN92, CN20, R412, R413, R418, R419, R426, R427.

DEL RN104, LN1, CN21, F2, L31, J1 (PS2).

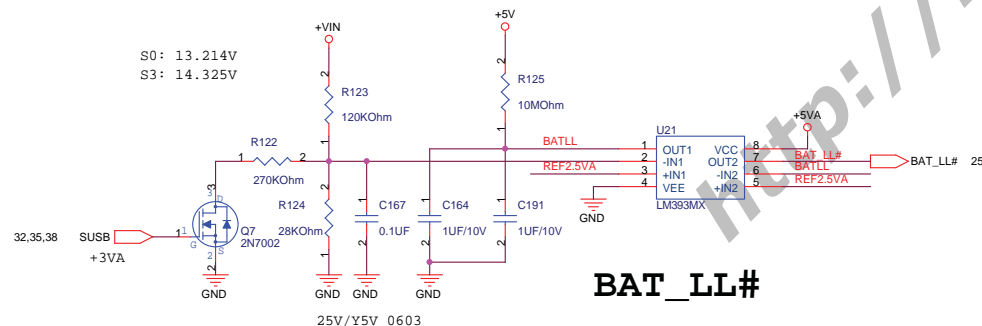
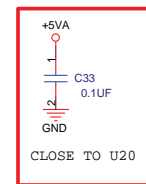
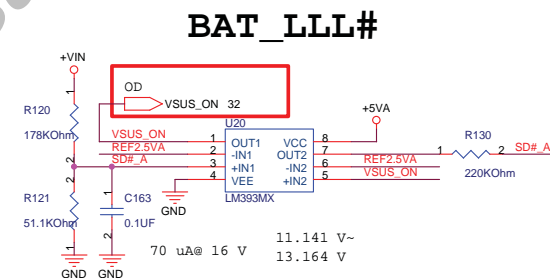
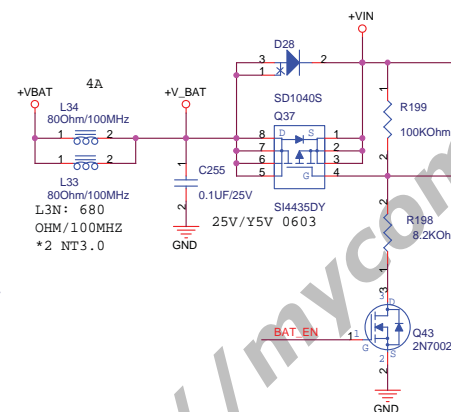
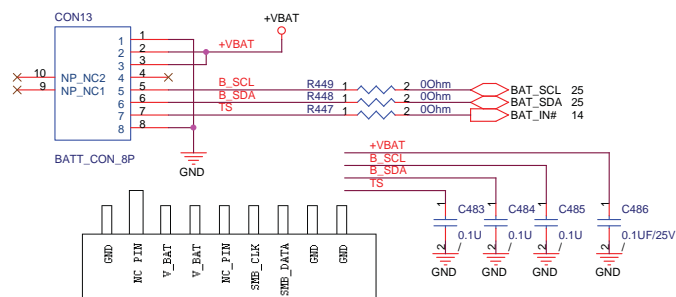
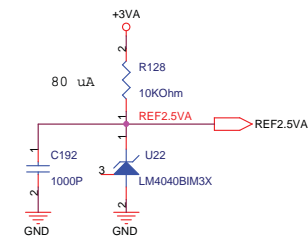




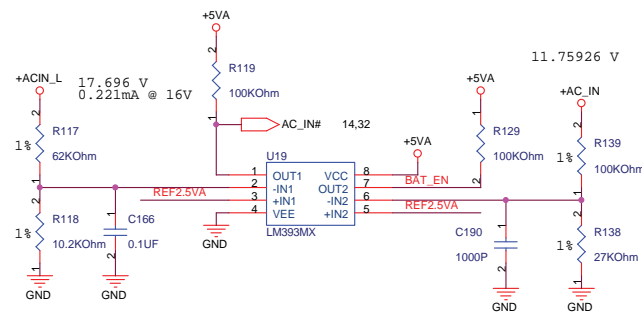




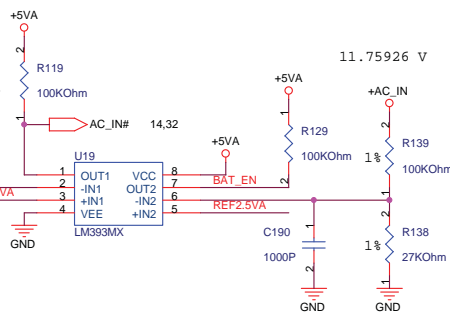
+VIN < 11.208 ~ 13.231 V: BAT_LLL#
 +AC_IN < 11.75926 V: BAT_EN
 +ACIN < 17.696 V: AC_IN
 +VIN < 13.214 V(S0), 14.325 V(S3): BAT_LL#



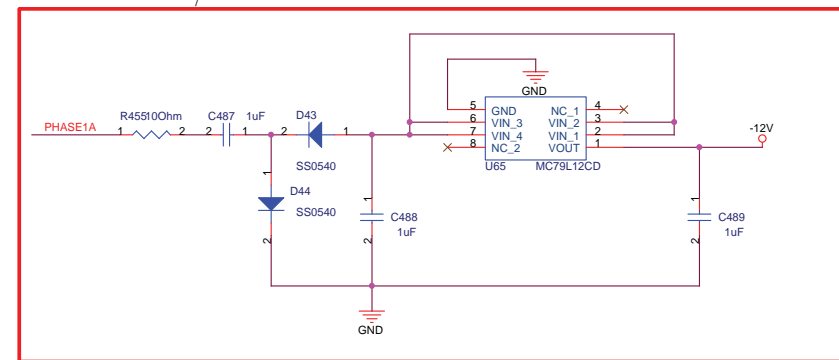
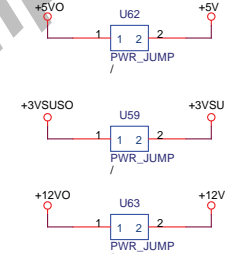
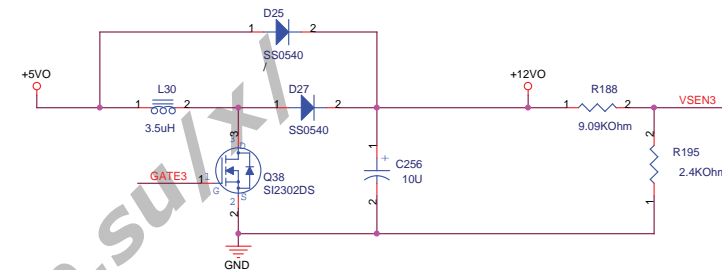
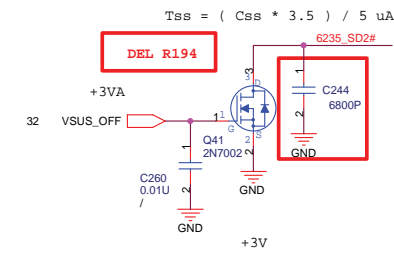
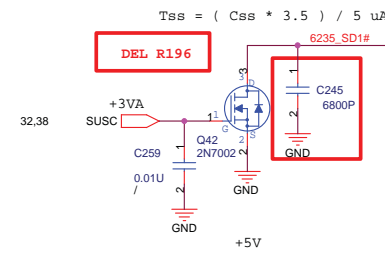
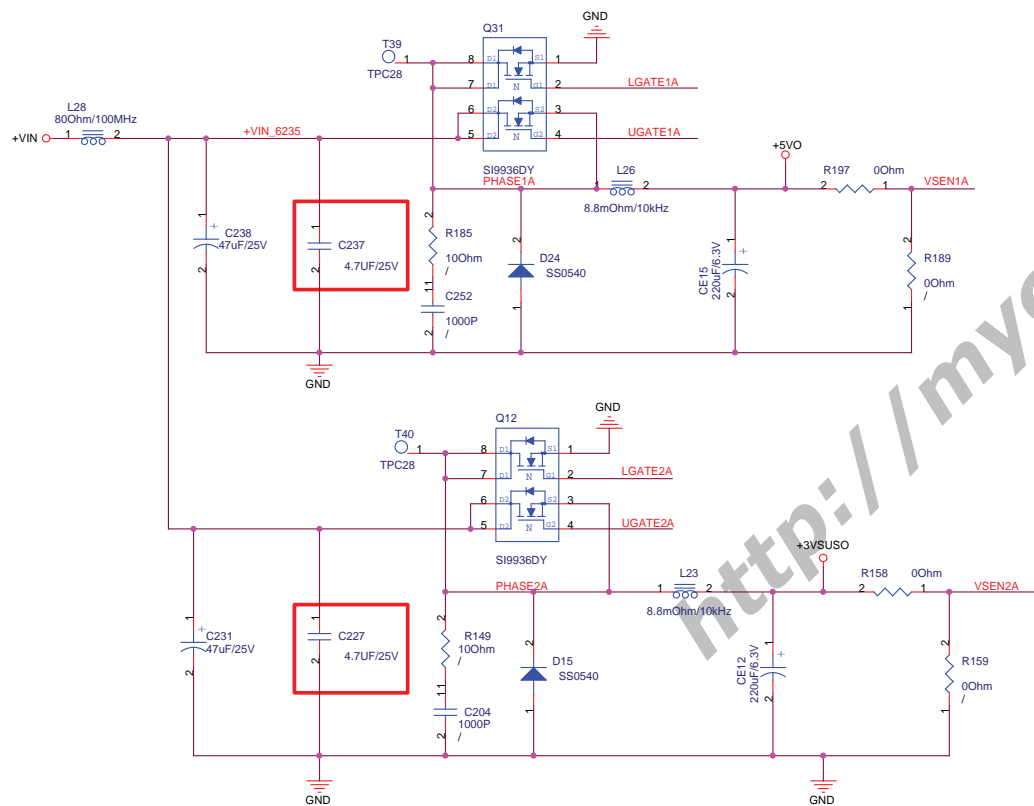
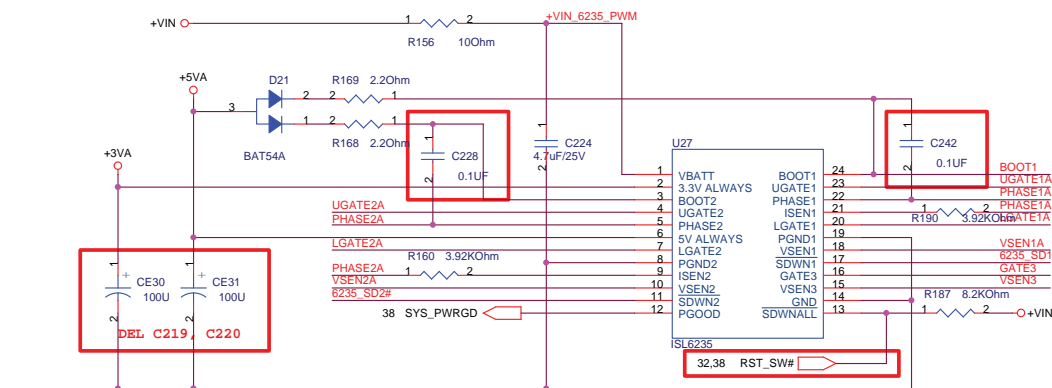
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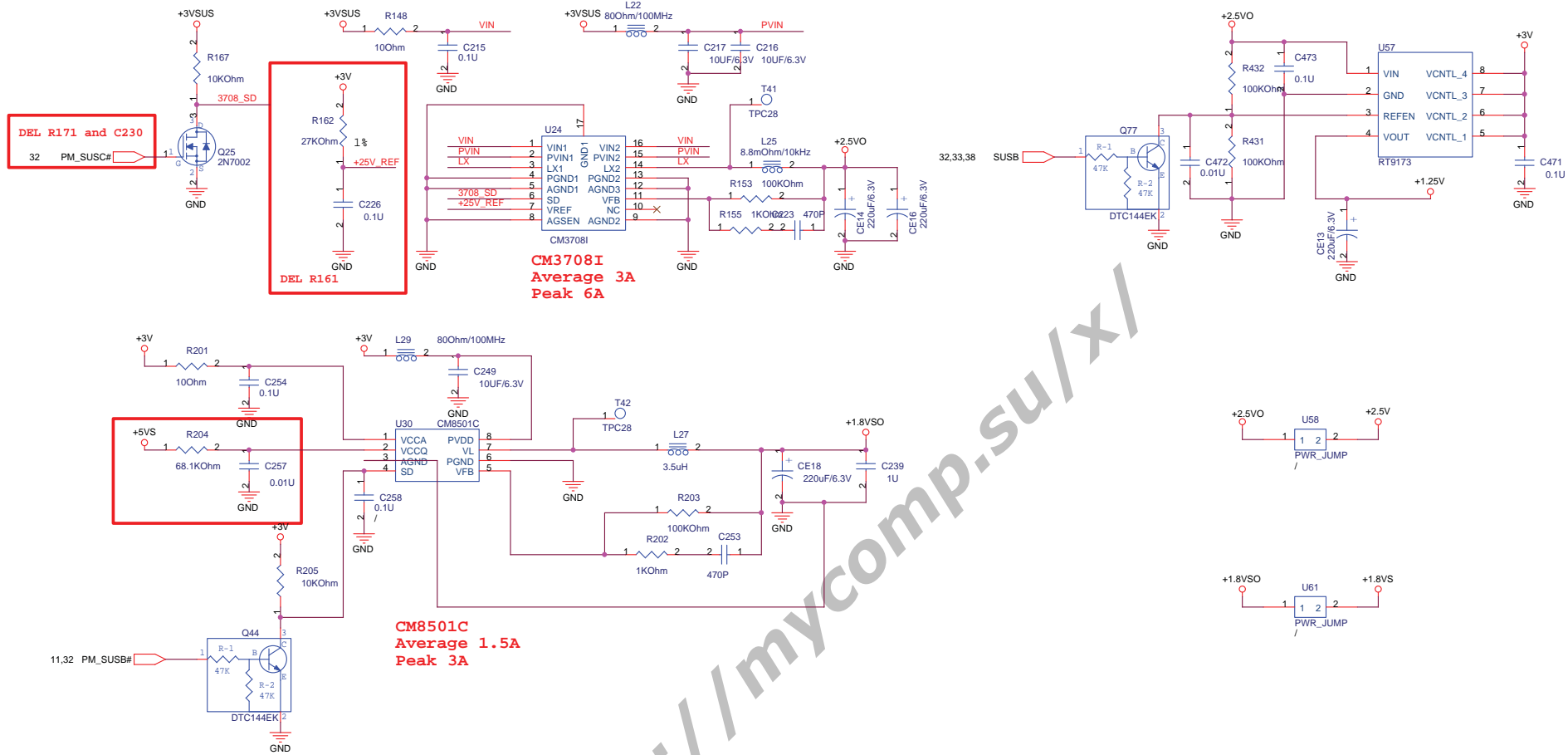


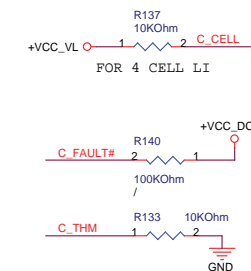
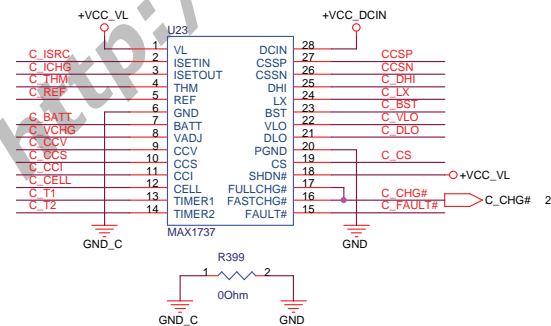
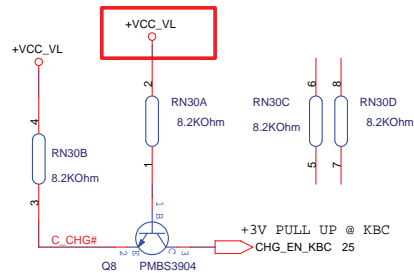
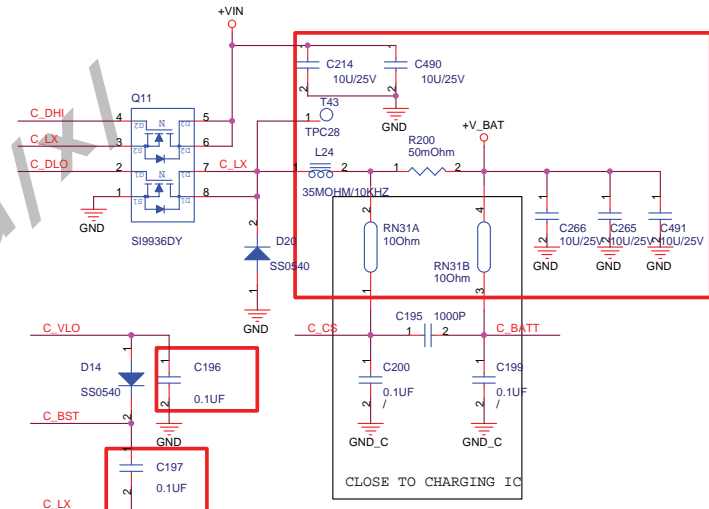
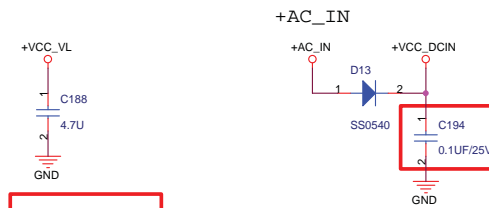
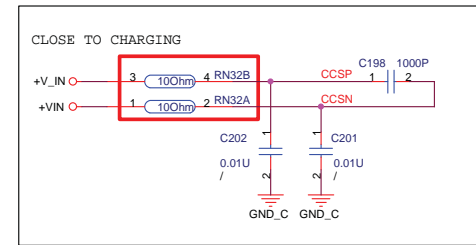
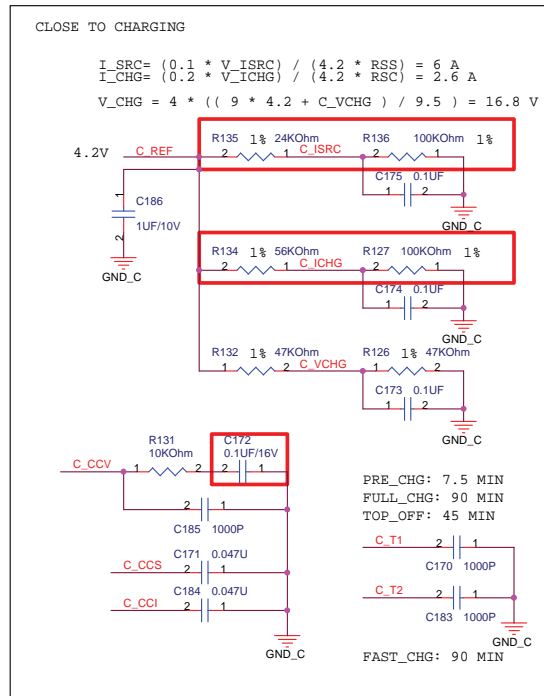
AC_IN#

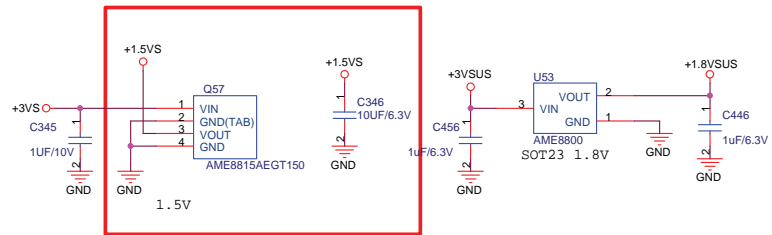


BAT_EN

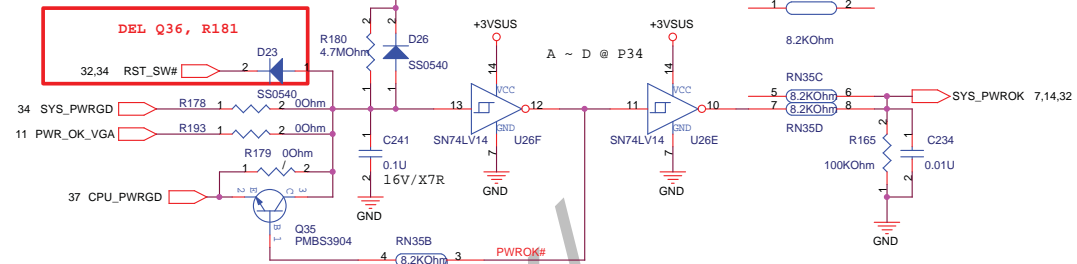
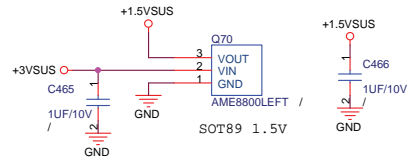






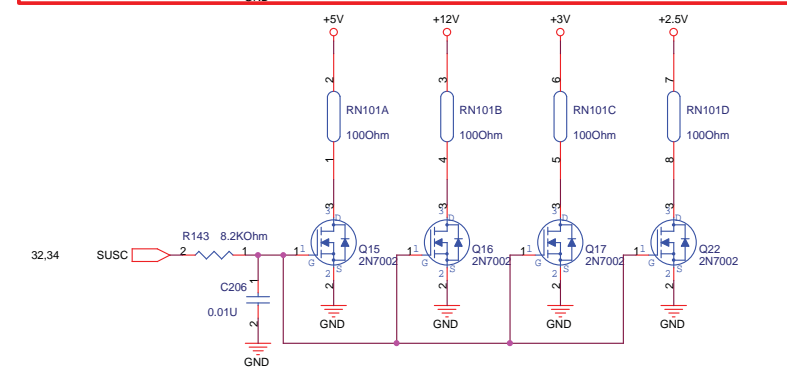
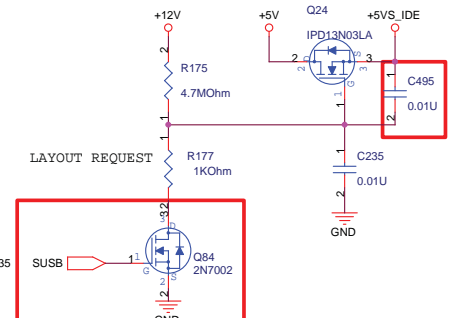
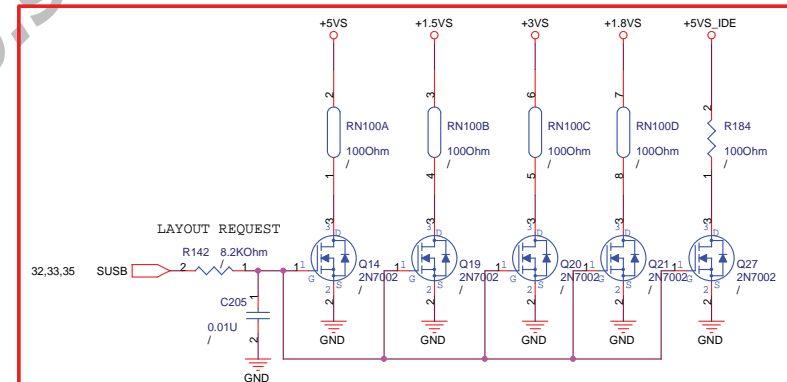
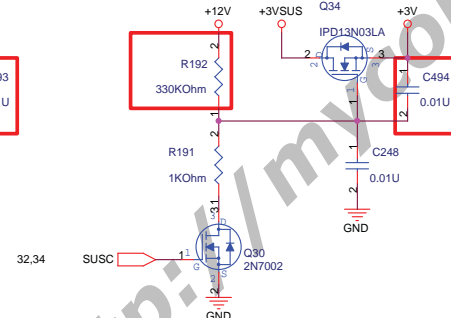
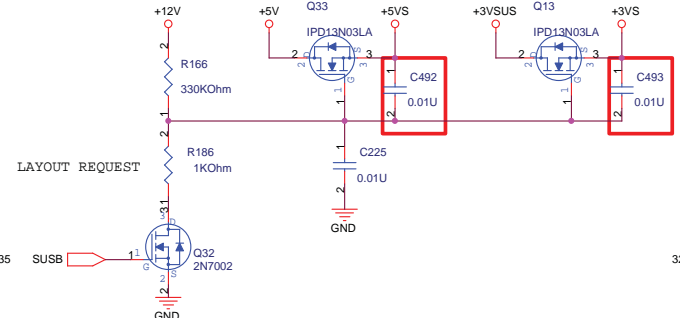


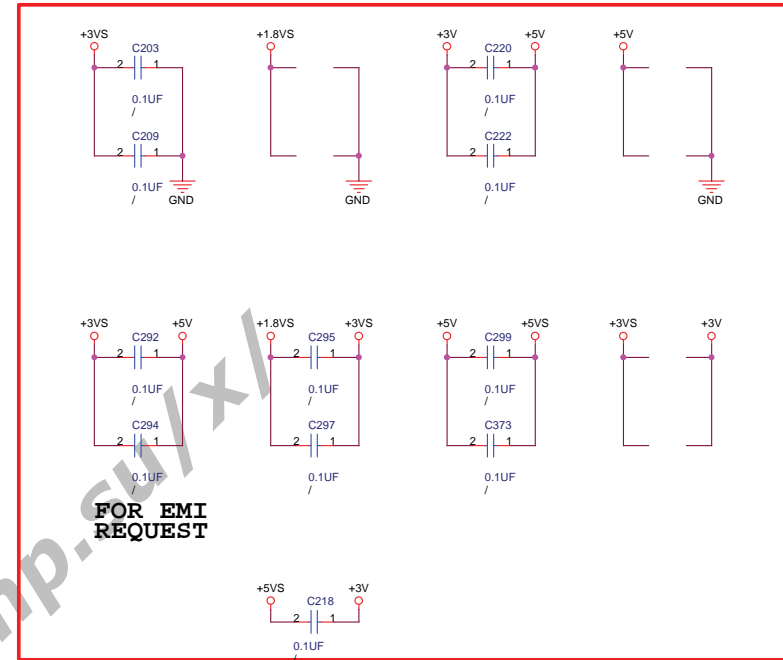
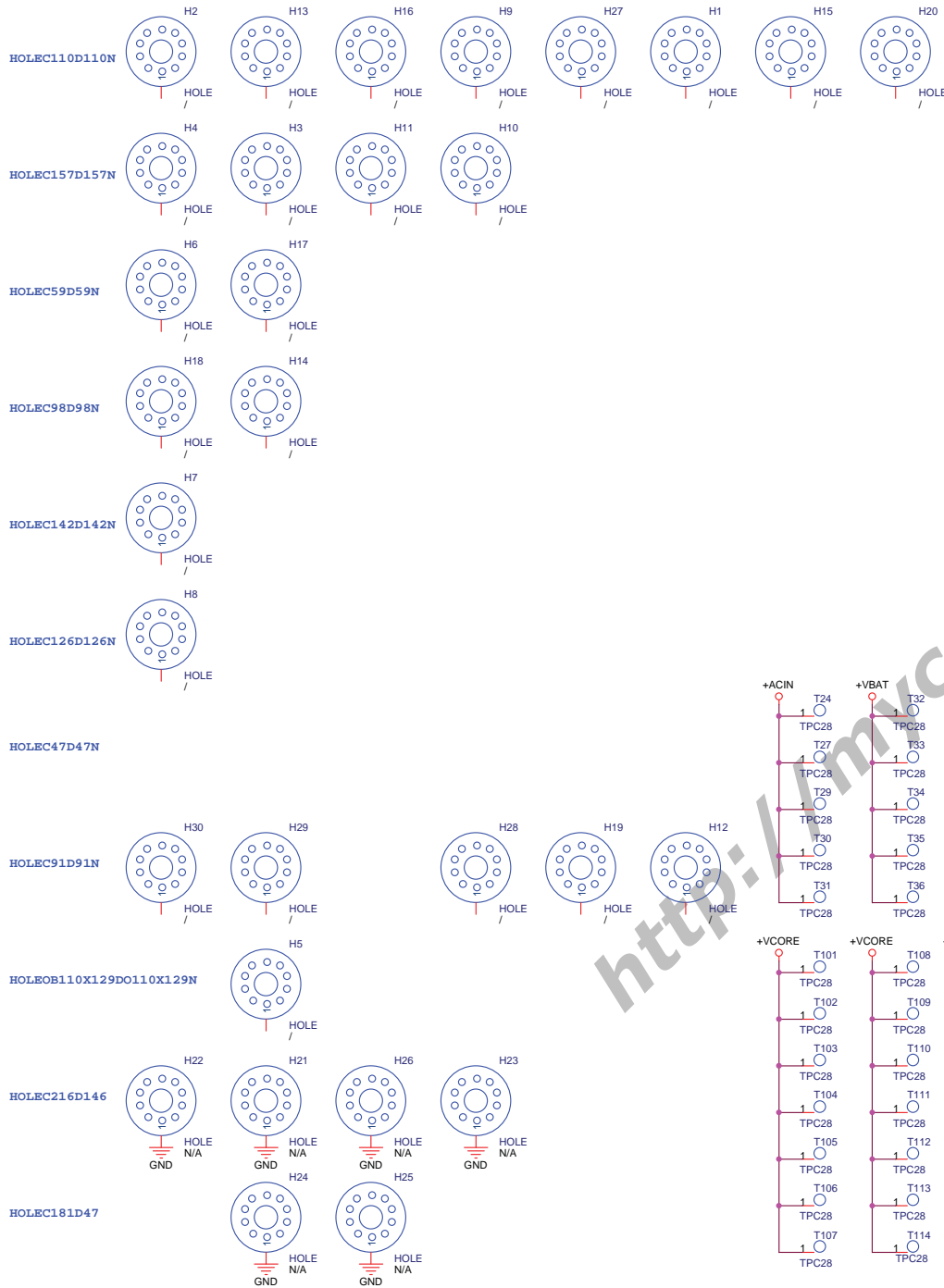
FOR SIS660 ONLY



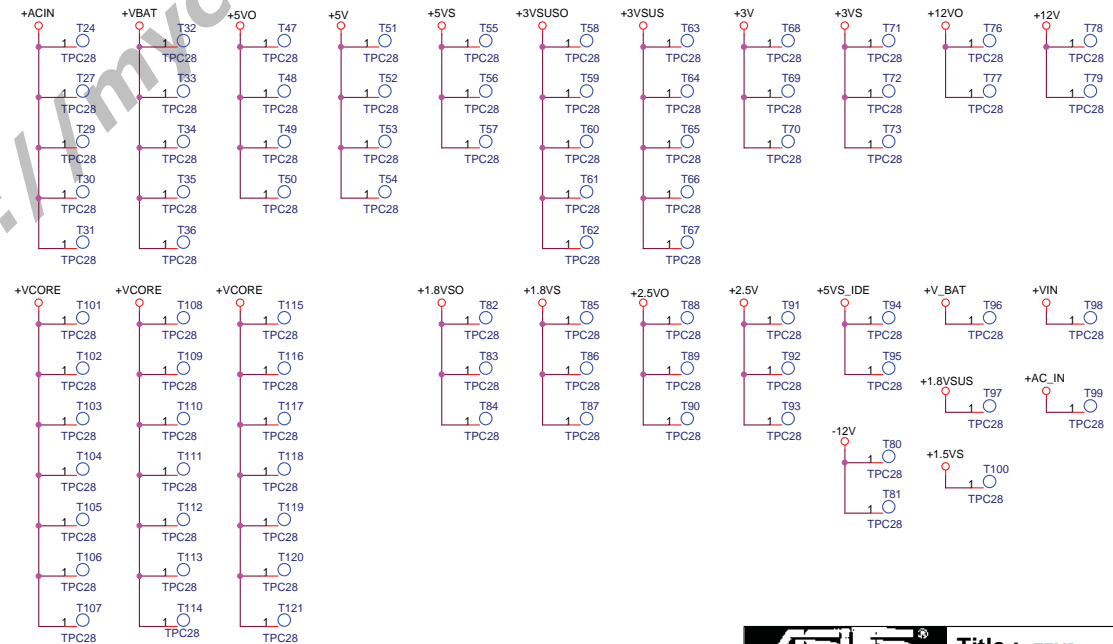
$$t = -\ln \left[\frac{V_{dd} - V_{th}}{V_{dd}} \right] \times T \quad (T = C \times R)$$

$$t = -\ln \left[\frac{5 - 2.3}{5} \right] \times 0.1 \mu F \times 470 M$$





FOR EMI
REQUEST



Rev	Date	Description
R1.00	02/10/24	1. Initial release.
R1.1	02/12/07	

Rev	Date	Description